

Memory Assessment of Versatile Video Coding

Arthur Cerveira (UFPel)

Luciano Agostini (UFPel)

Bruno Zatt (UFPel)

Felipe Sampaio (IFRS - Campus Farroupilha)

aacerveira@inf.ufpel.edu.br



UFPEL



INSTITUTO FEDERAL

Rio Grande do Sul

Campus Farroupilha

Introduction

- Background
- The novel Versatile Video Coding (VVC)
- Goal of this work
- Main contributions
 - *Memory profiling*
 - *Inter-prediction memory analysis*

Introduction

- Background
- **The novel Versatile Video Coding (VVC)**
- Goal of this work
- Main contributions
 - *Memory profiling*
 - *Inter-prediction memory analysis*

Introduction

- Background
- The novel Versatile Video Coding (VVC)
- **Goal of this work**
- Main contributions
 - *Memory profiling*
 - *Inter-prediction memory analysis*

Introduction

- Background
- The novel Versatile Video Coding (VVC)
- Goal of this work
- **Main contributions**
 - *Memory profiling*
 - *Inter-prediction memory analysis*

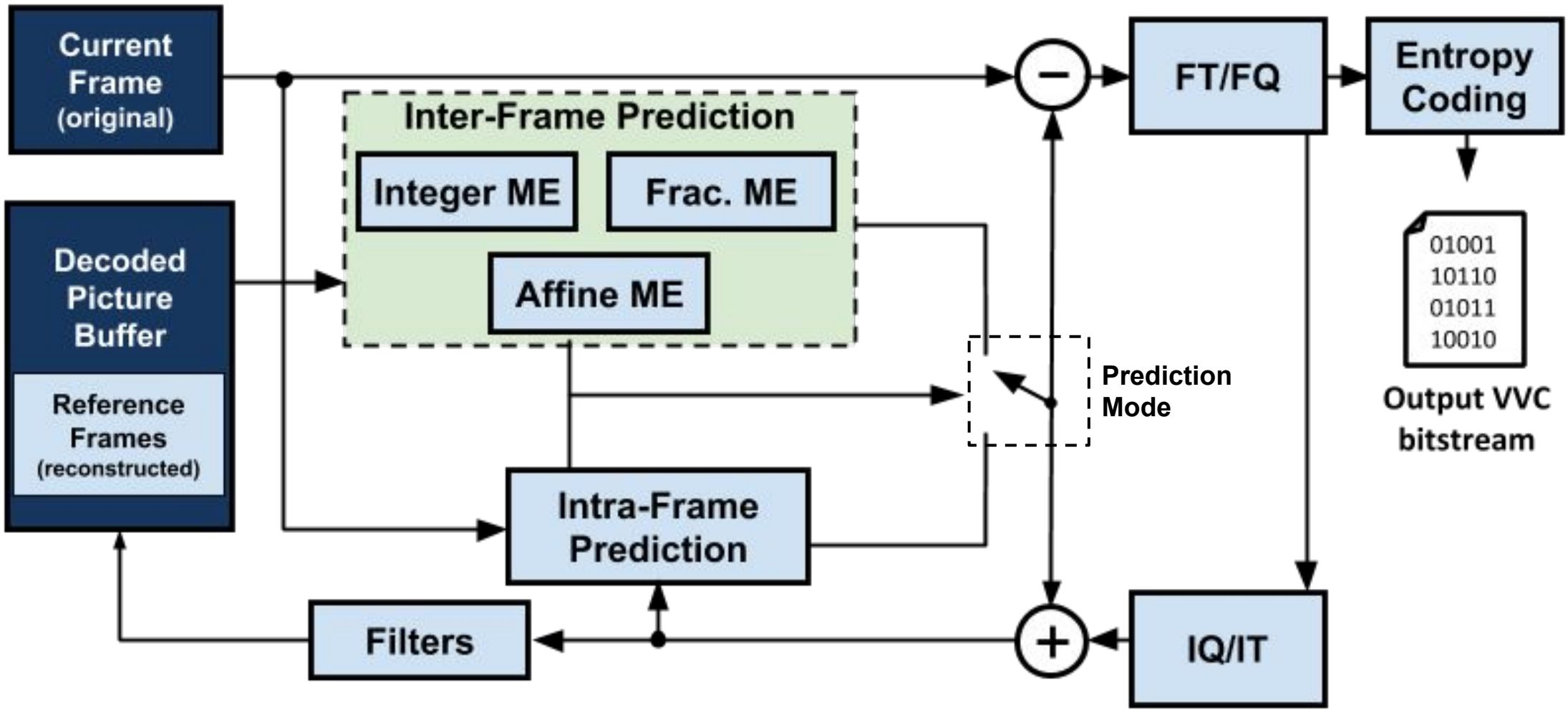
Introduction

- Background
- The novel Versatile Video Coding (VVC)
- Goal of this work
- Main contributions
 - *Memory profiling*
 - *Inter-prediction memory analysis*

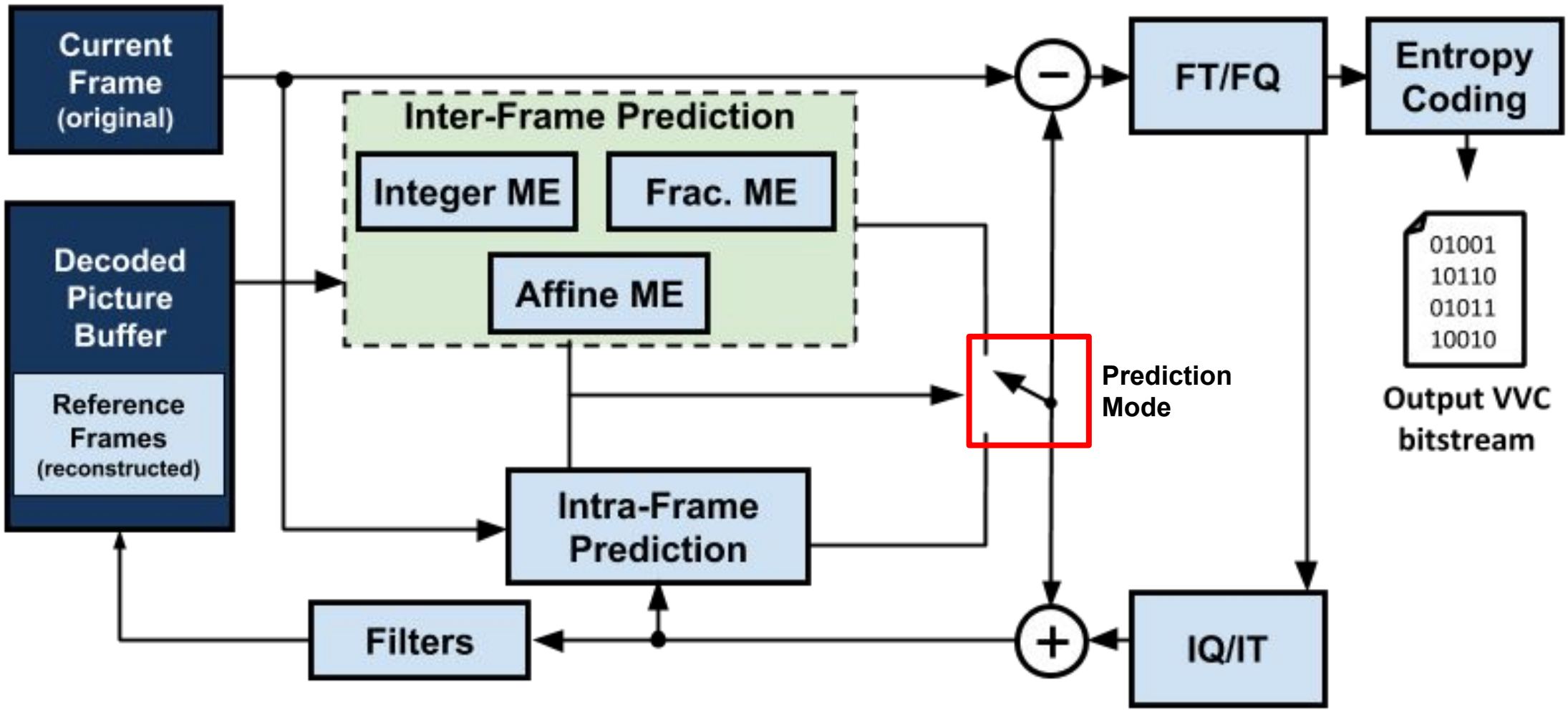
Introduction

- Background
- The novel Versatile Video Coding (VVC)
- Goal of this work
- Main contributions
 - *Memory profiling*
 - *Inter-prediction memory analysis*

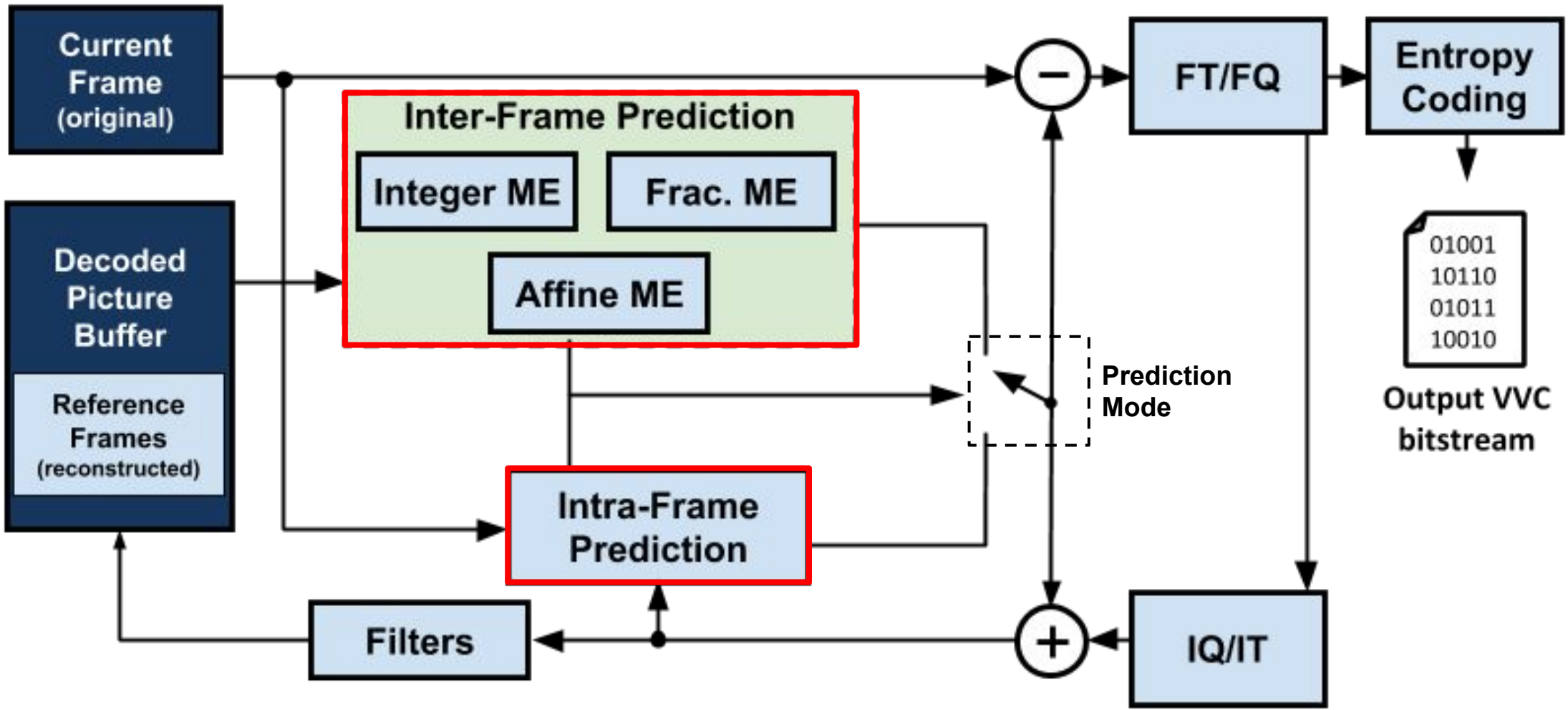
VVC Encoder



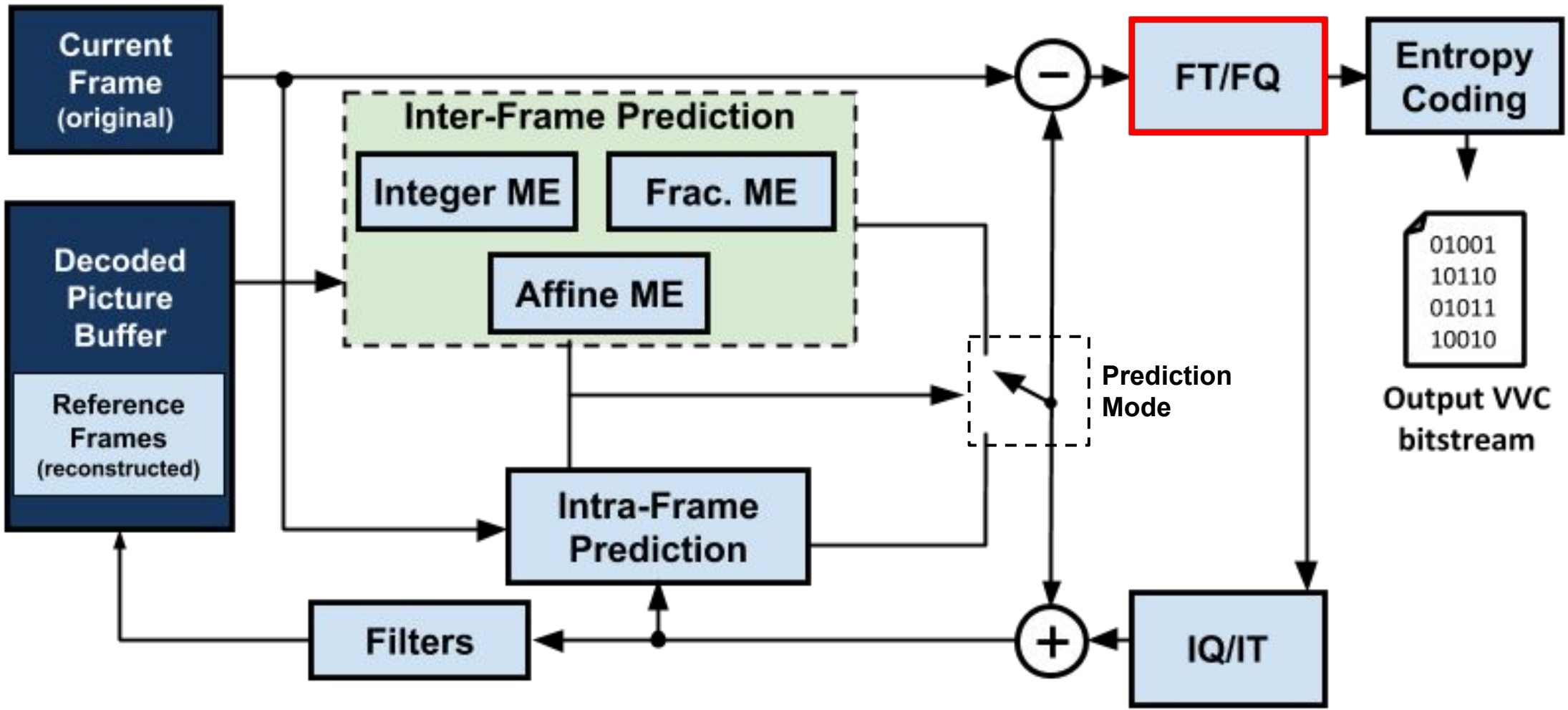
VVC Encoder



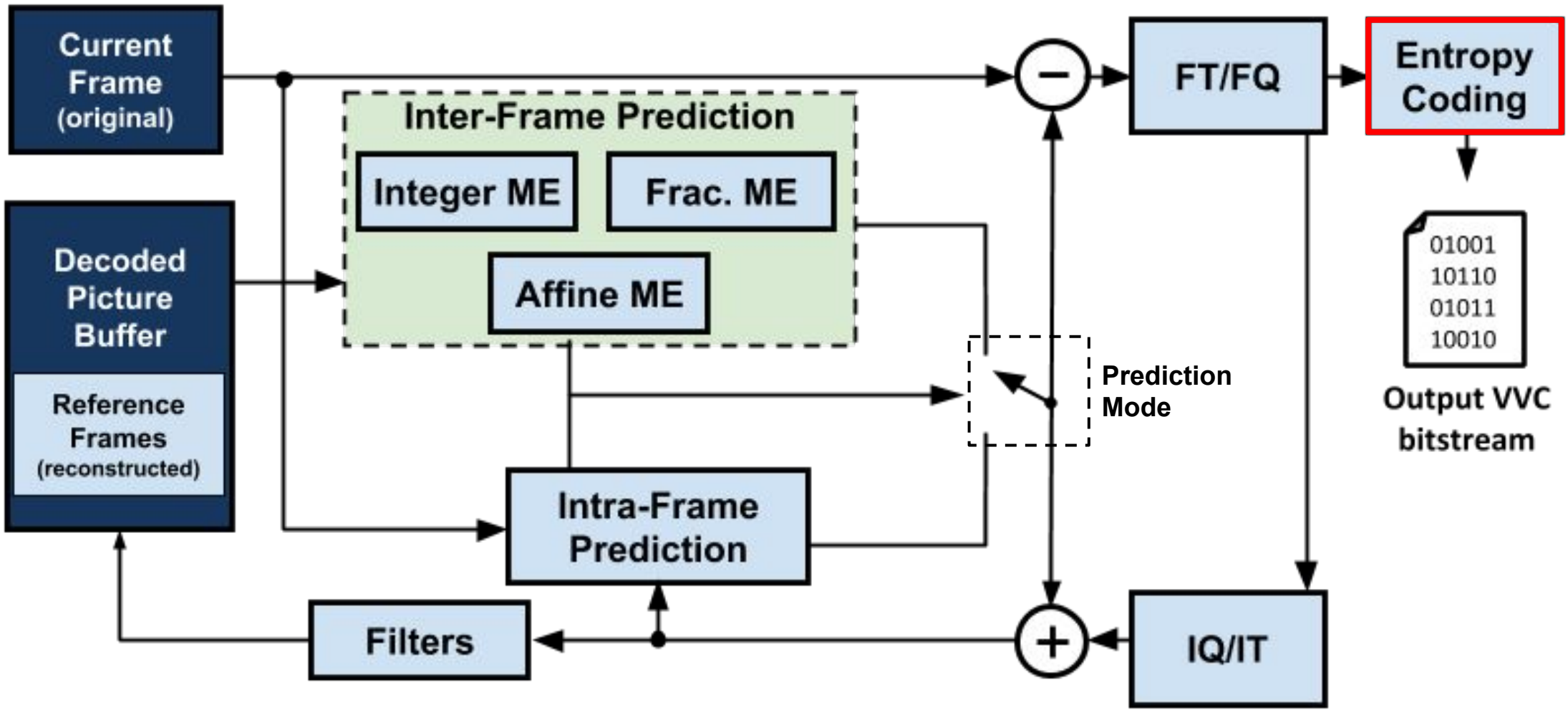
VVC Encoder



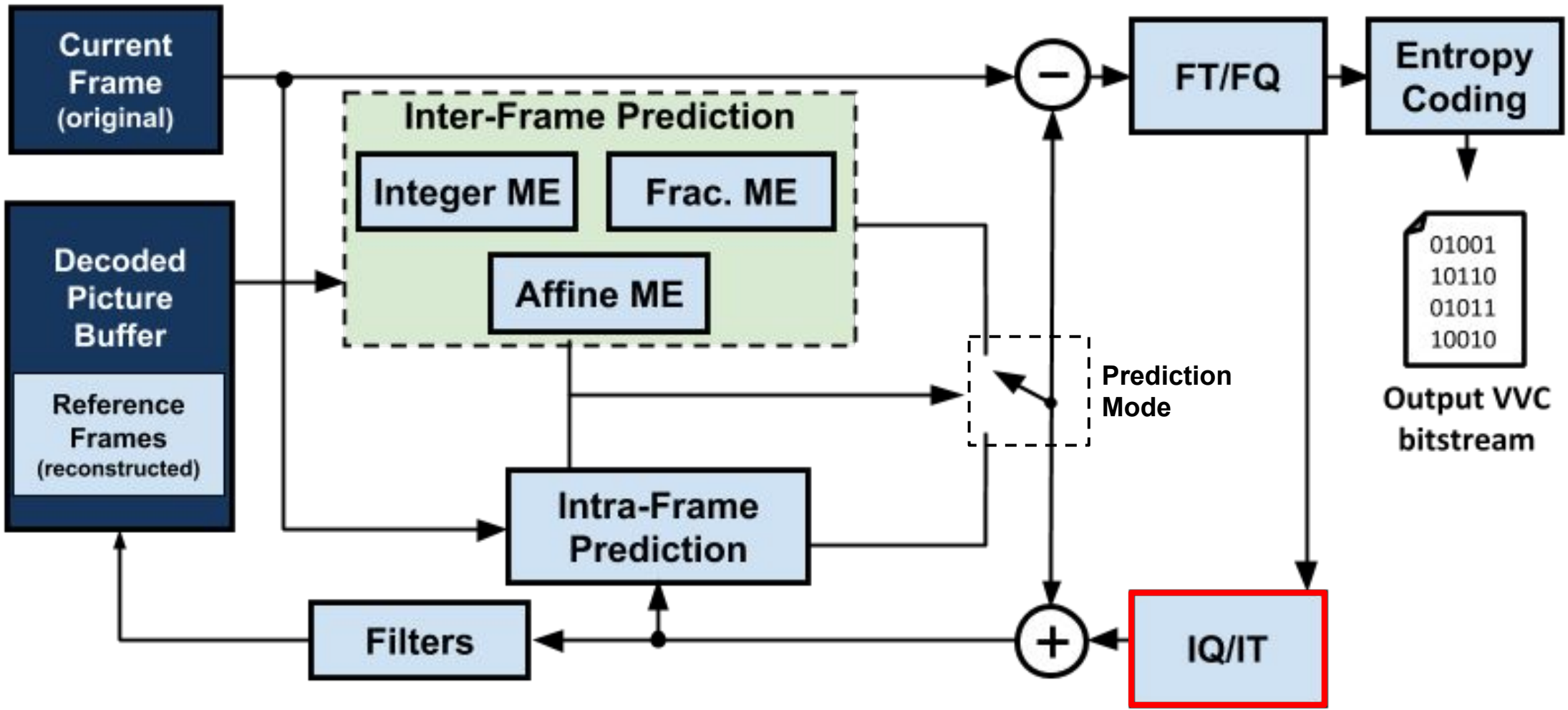
VVC Encoder



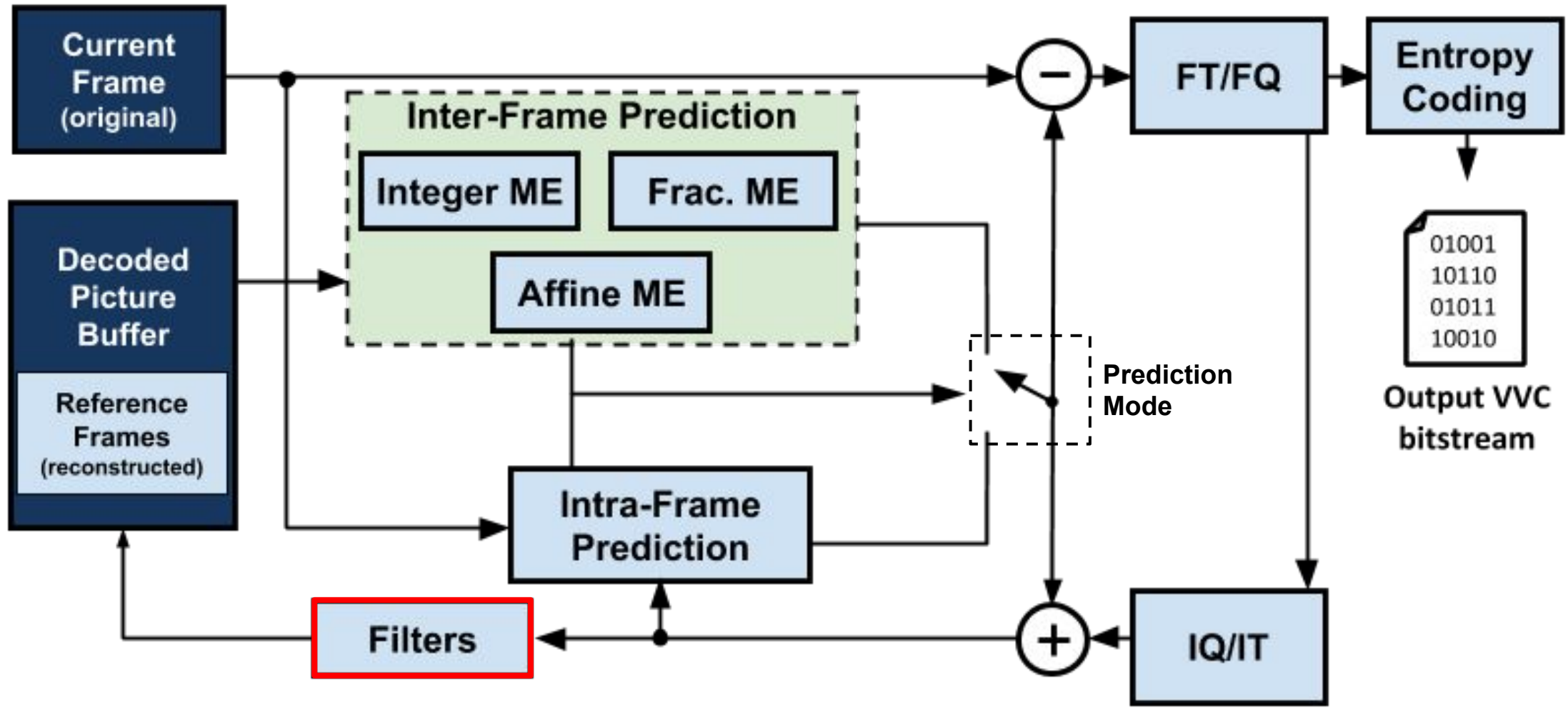
VVC Encoder



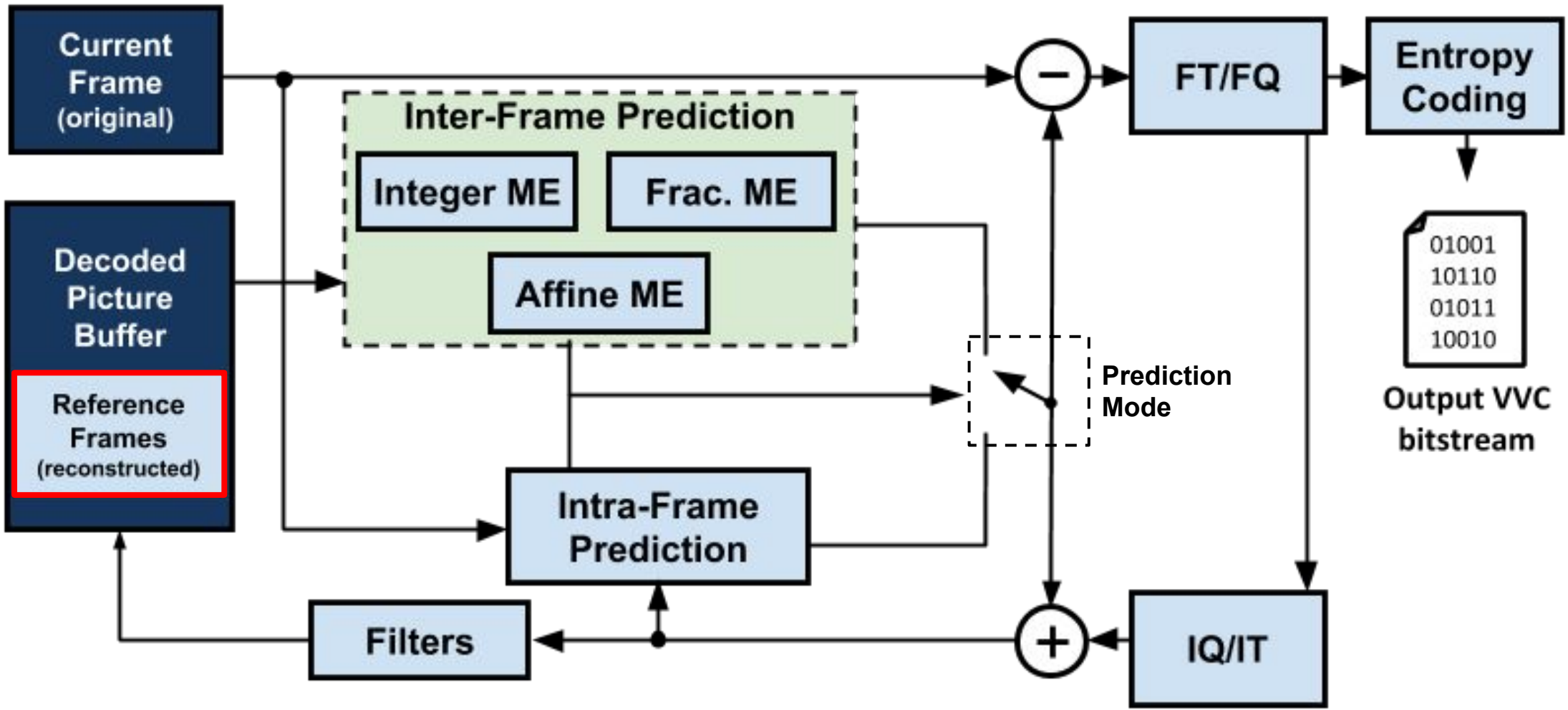
VVC Encoder



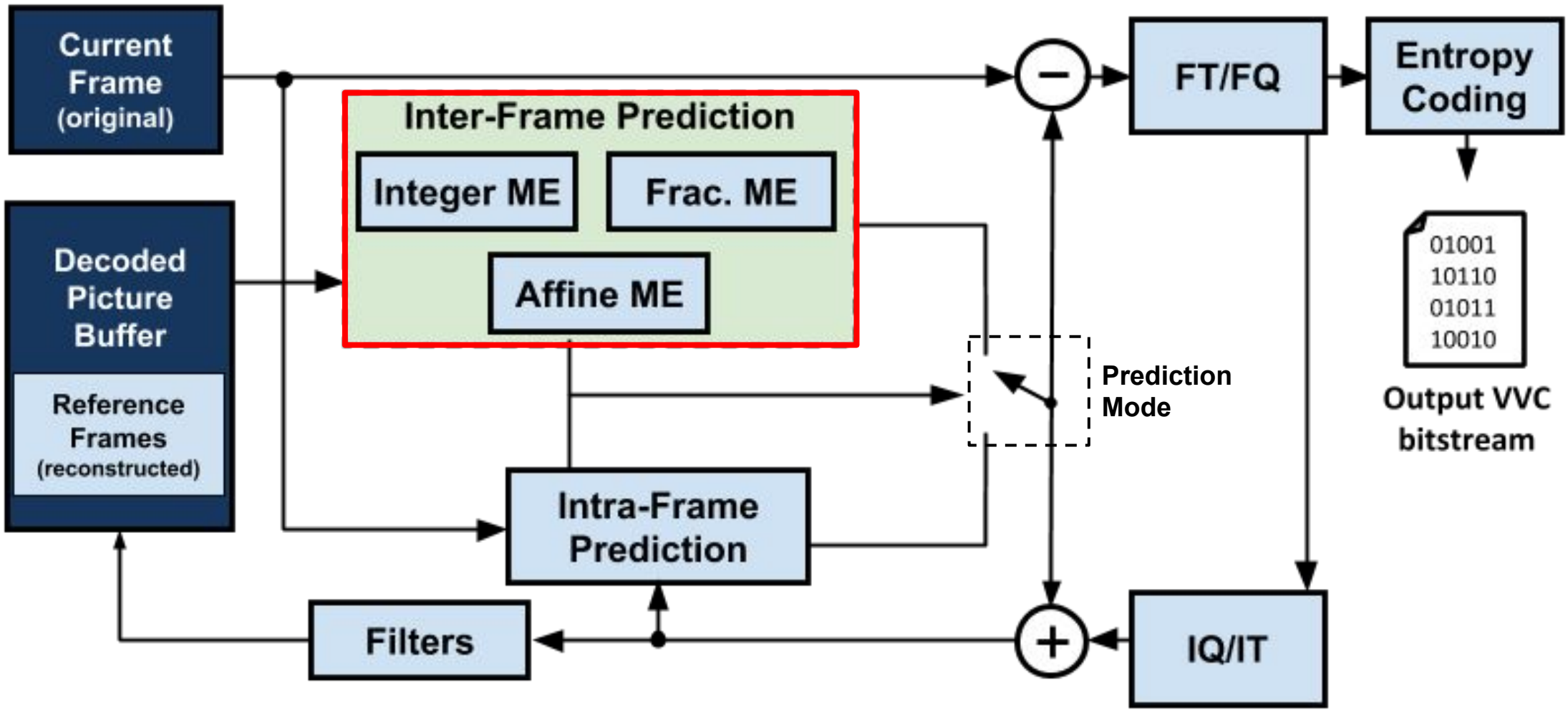
VVC Encoder



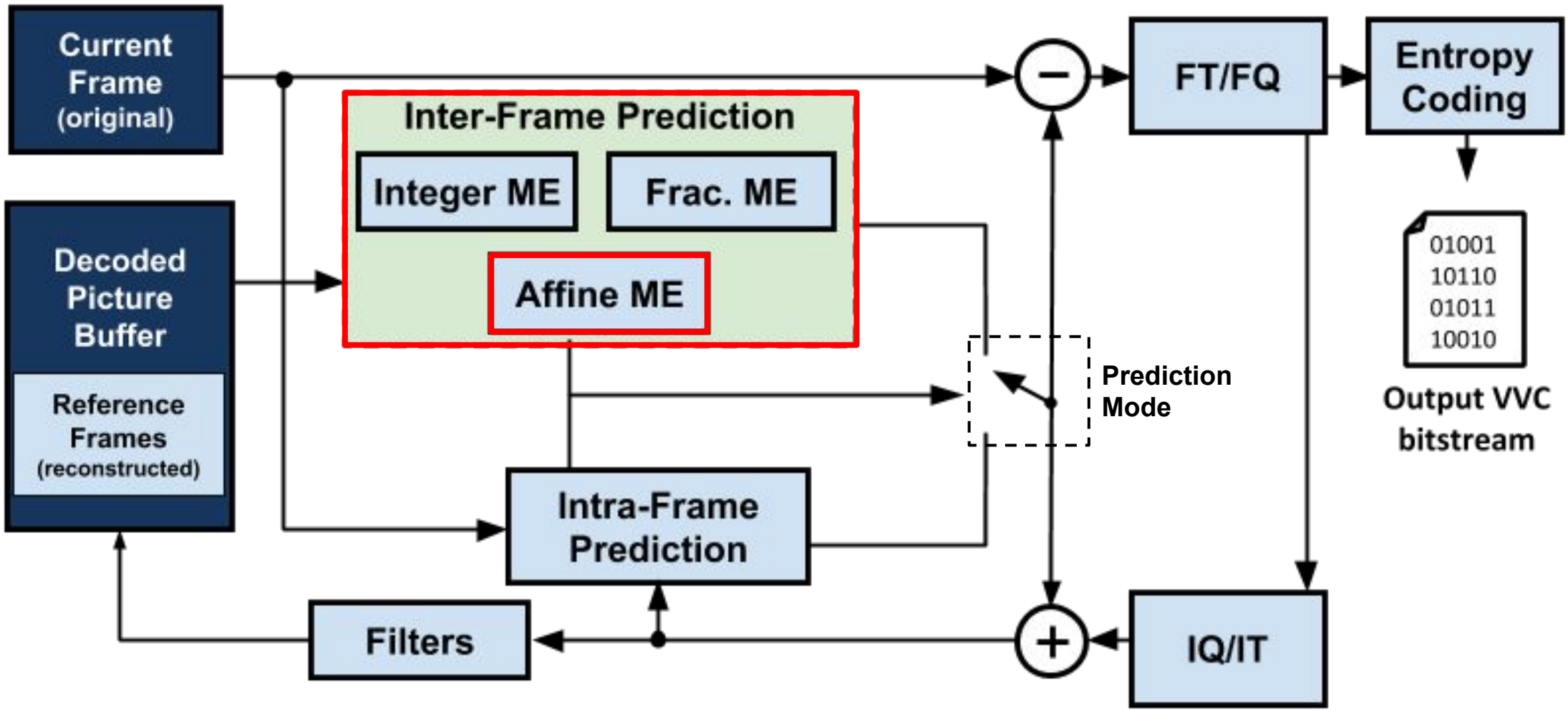
VVC Encoder



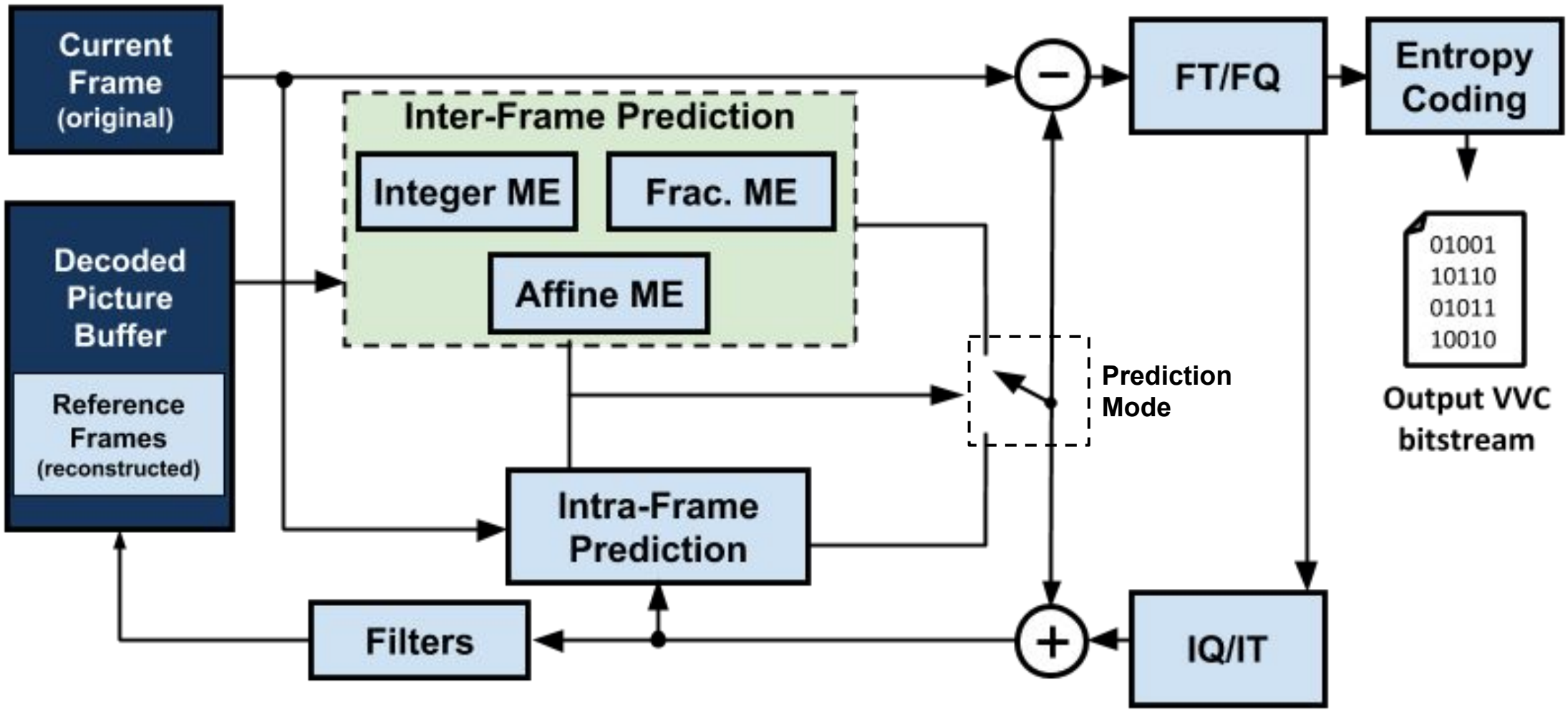
VVC Encoder



VVC Encoder

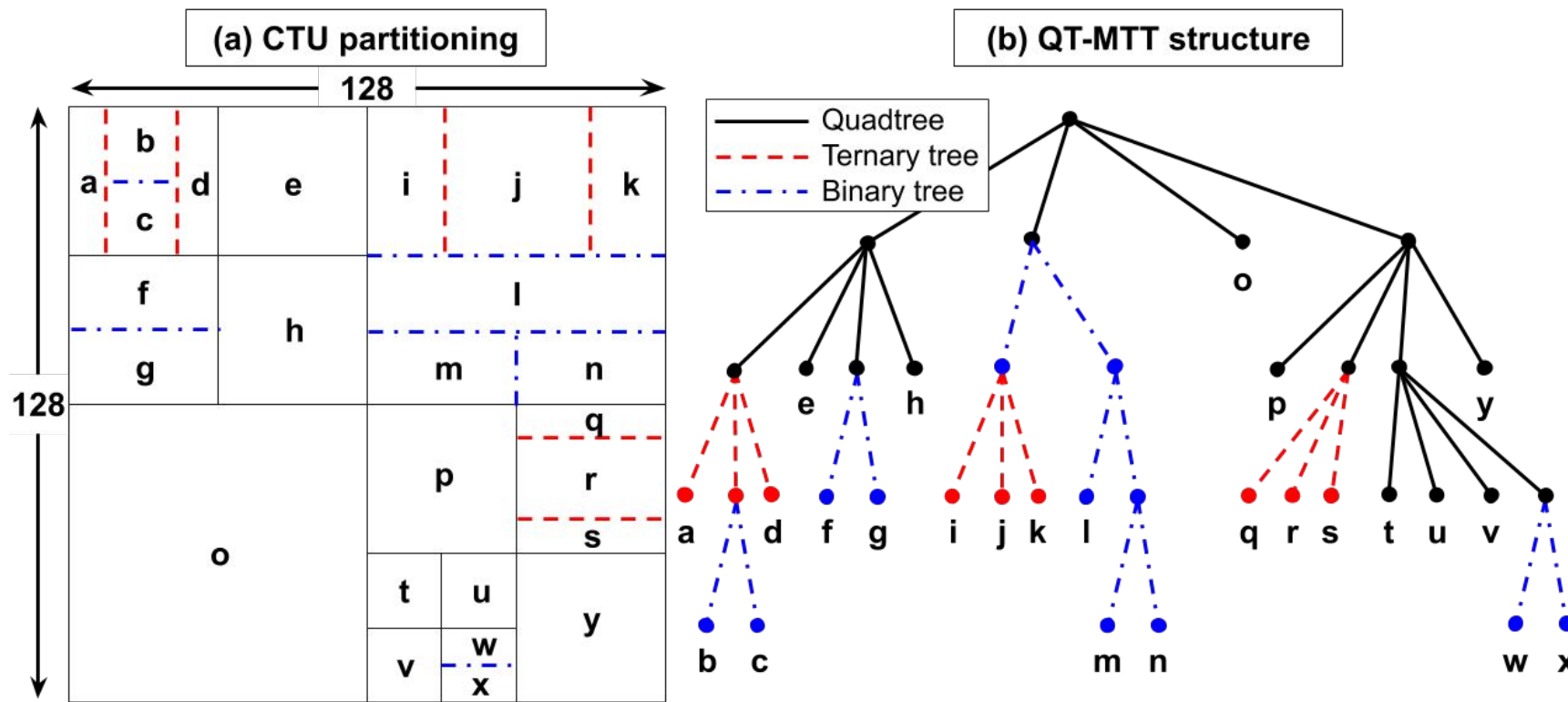


VVC Encoder



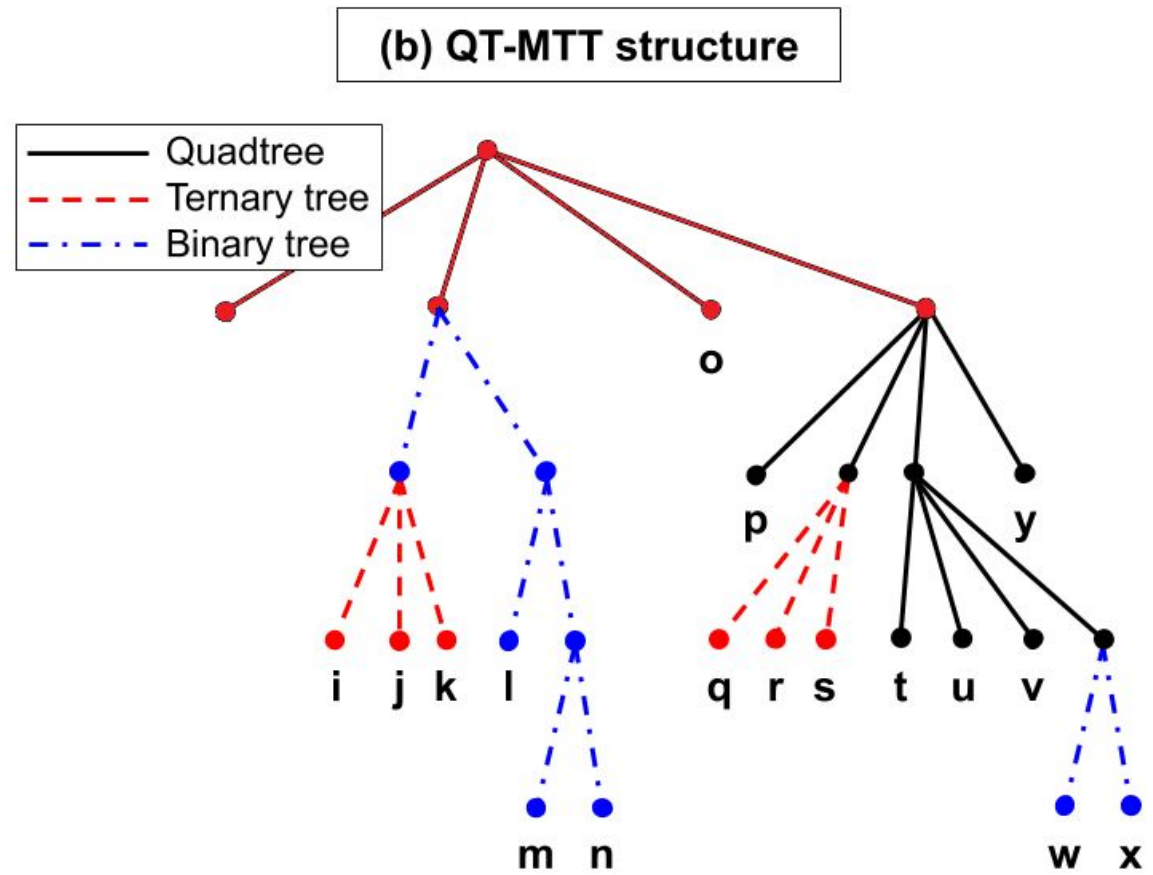
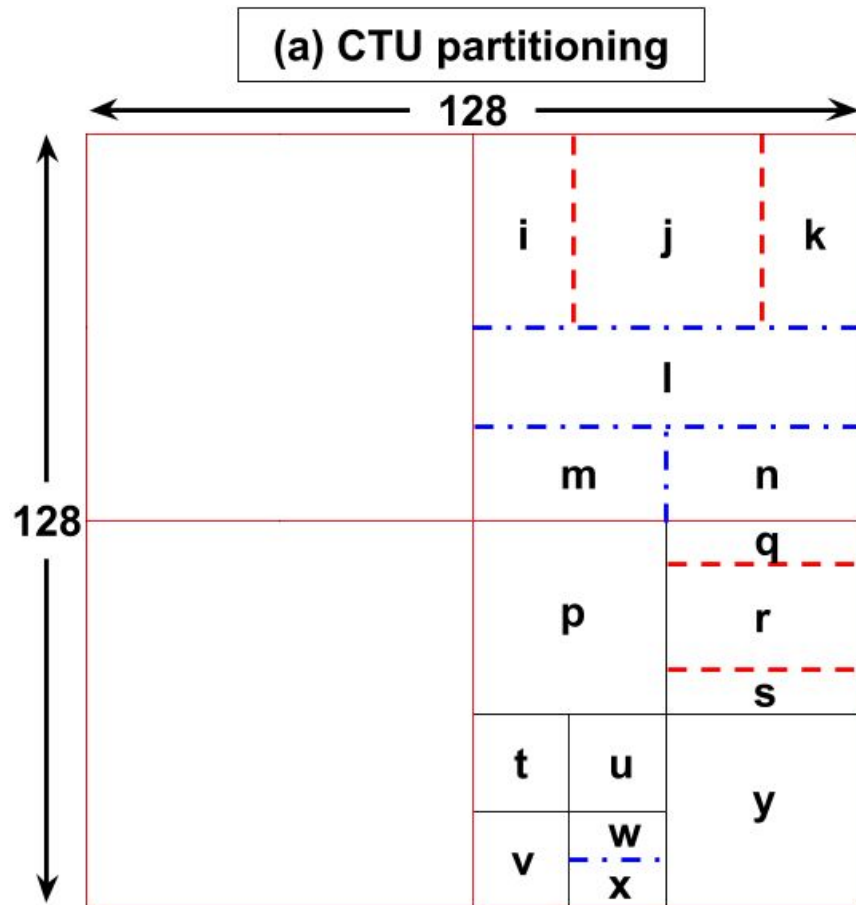
VVC Coding Structures

- Main innovations of VVC
- QT-MTT structure



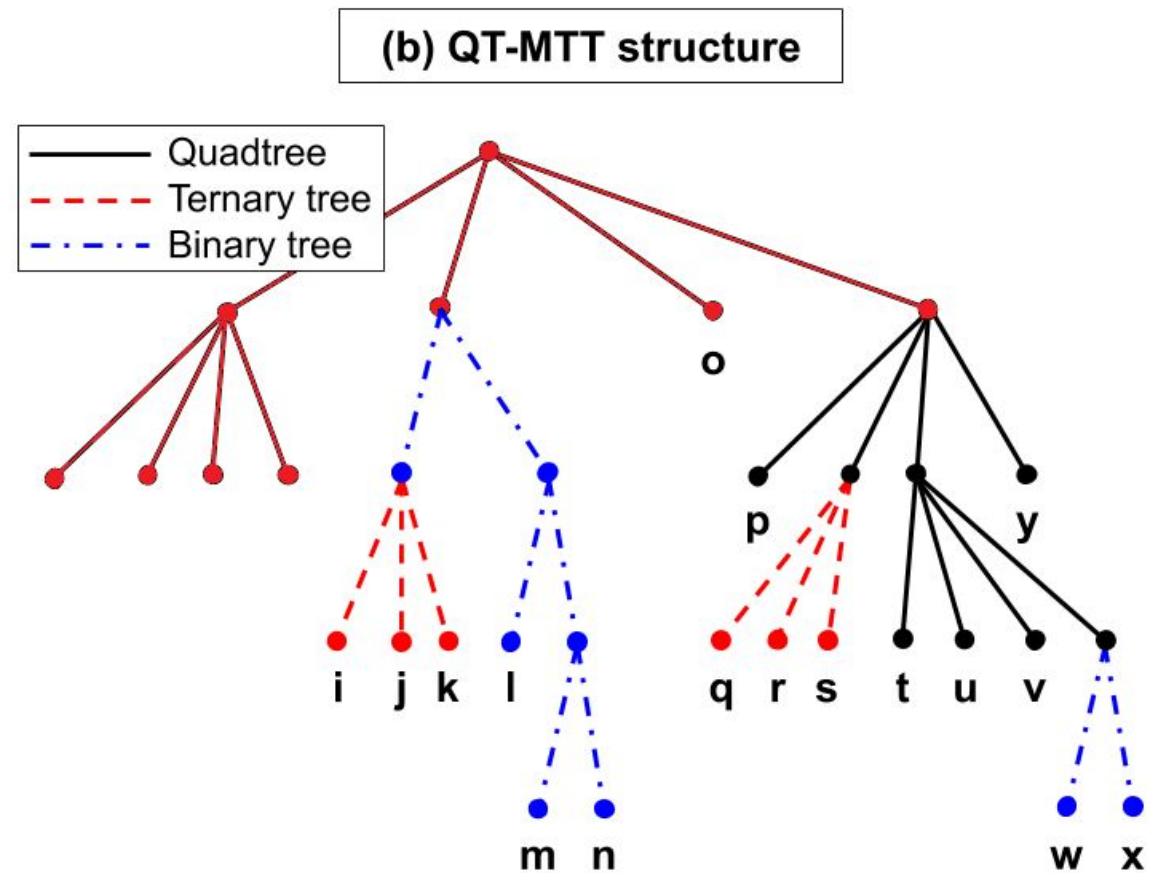
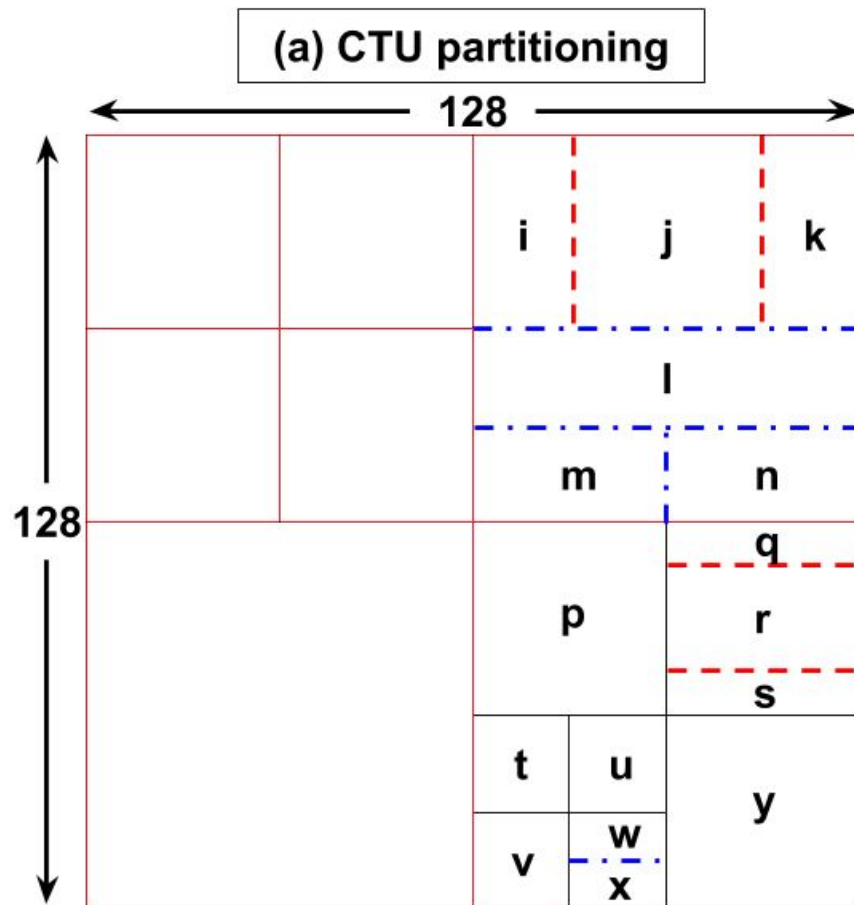
VVC Coding Structures

- QT-MTT example



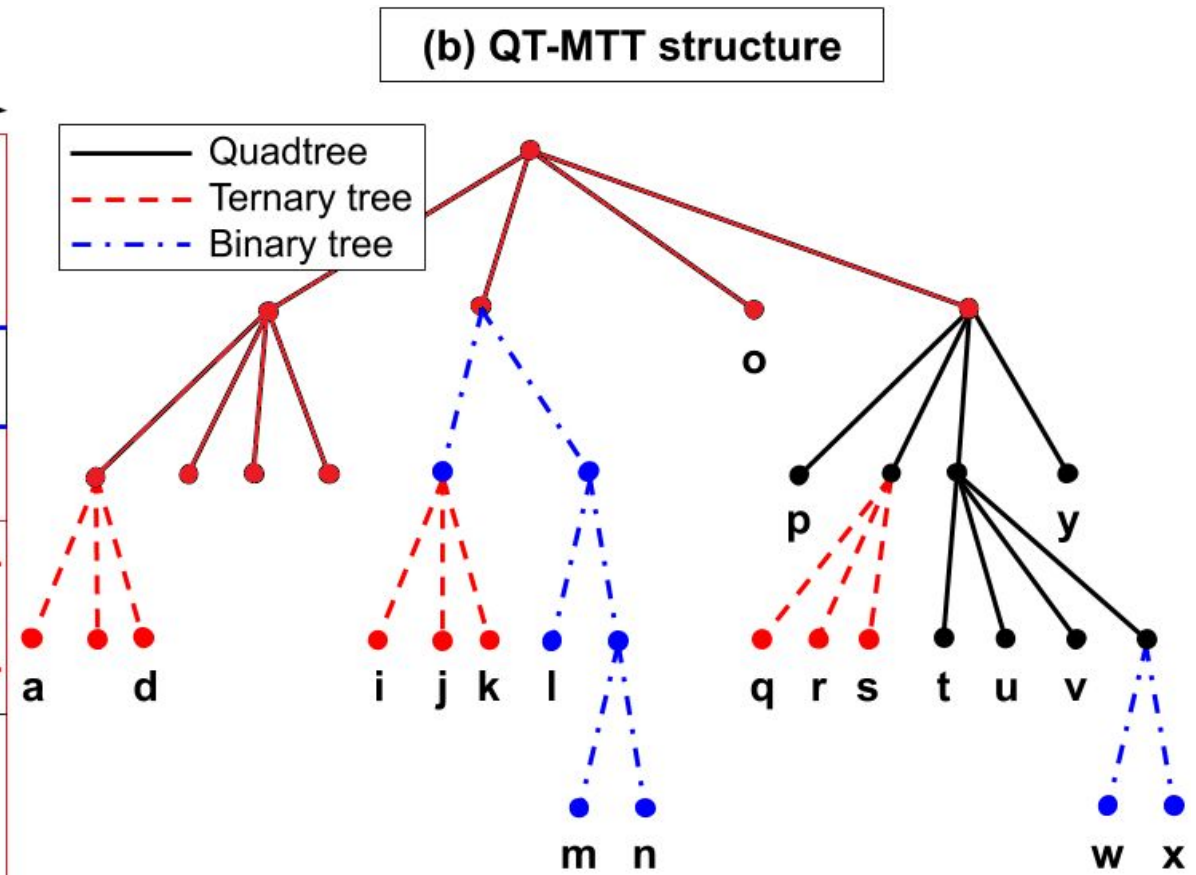
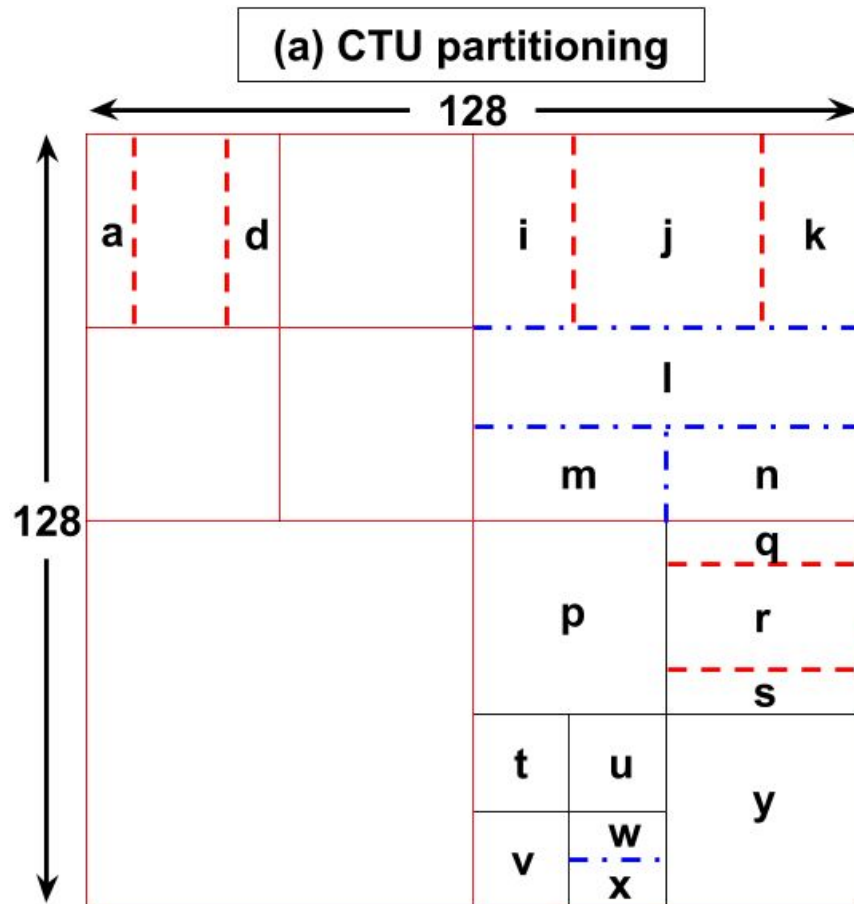
VVC Coding Structures

- QT-MTT example



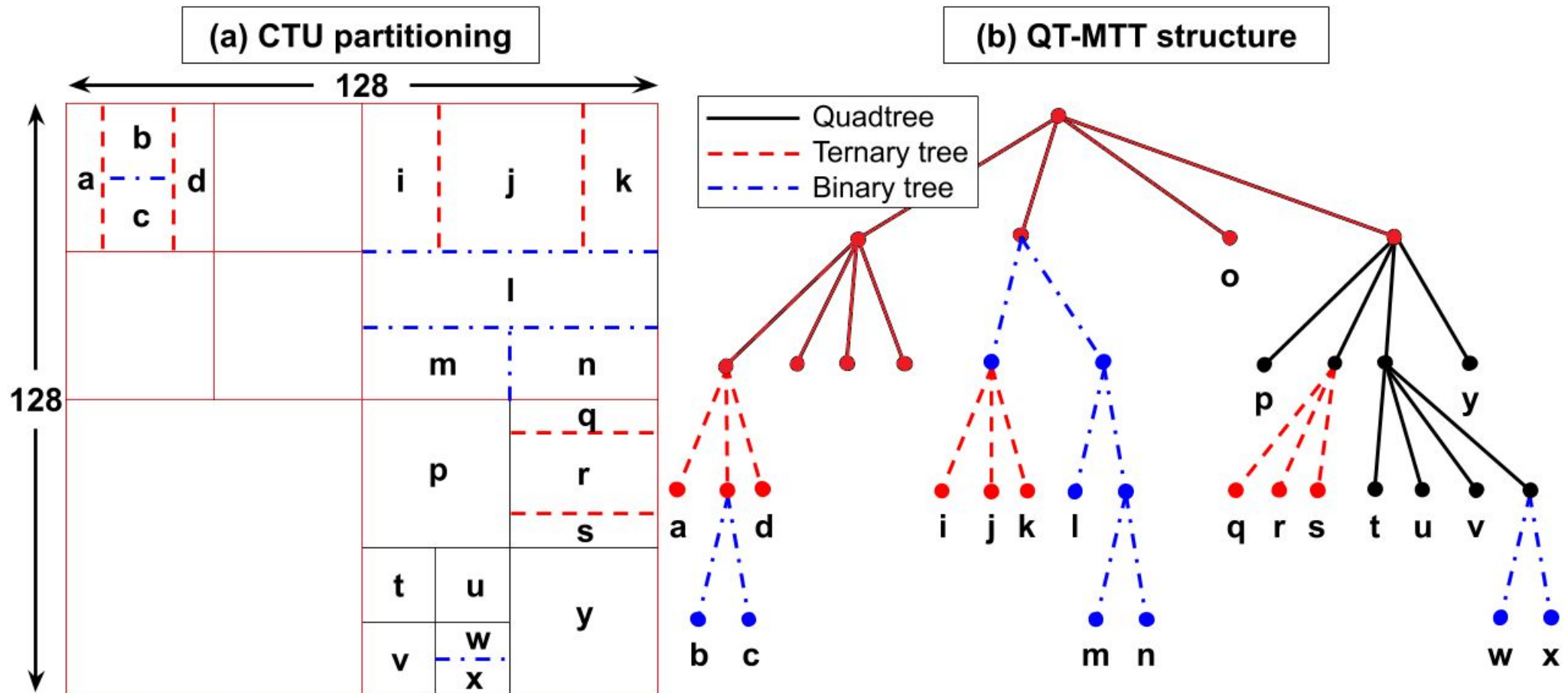
VVC Coding Structures

- QT-MTT example



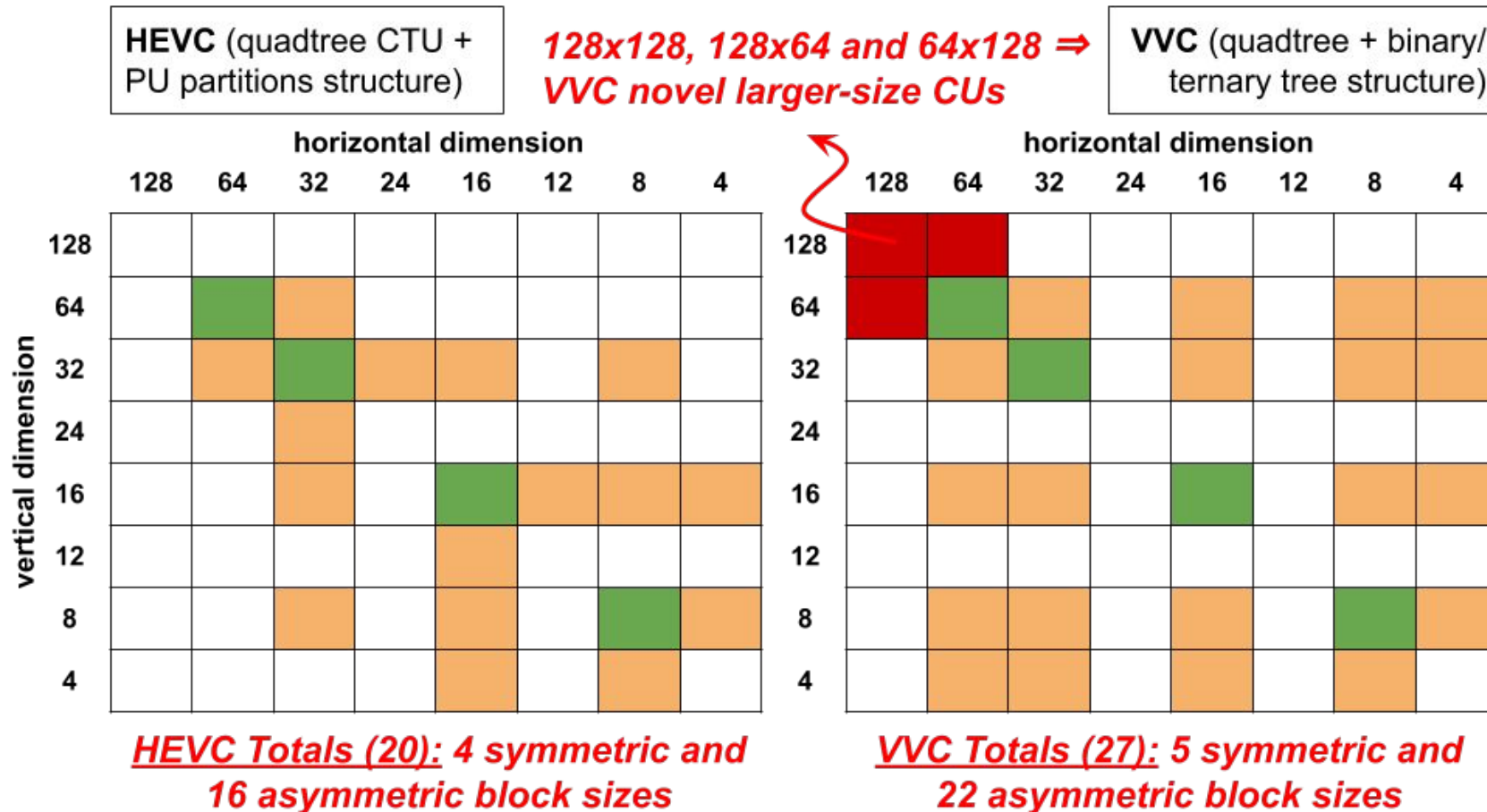
VVC Coding Structures

- QT-MTT example



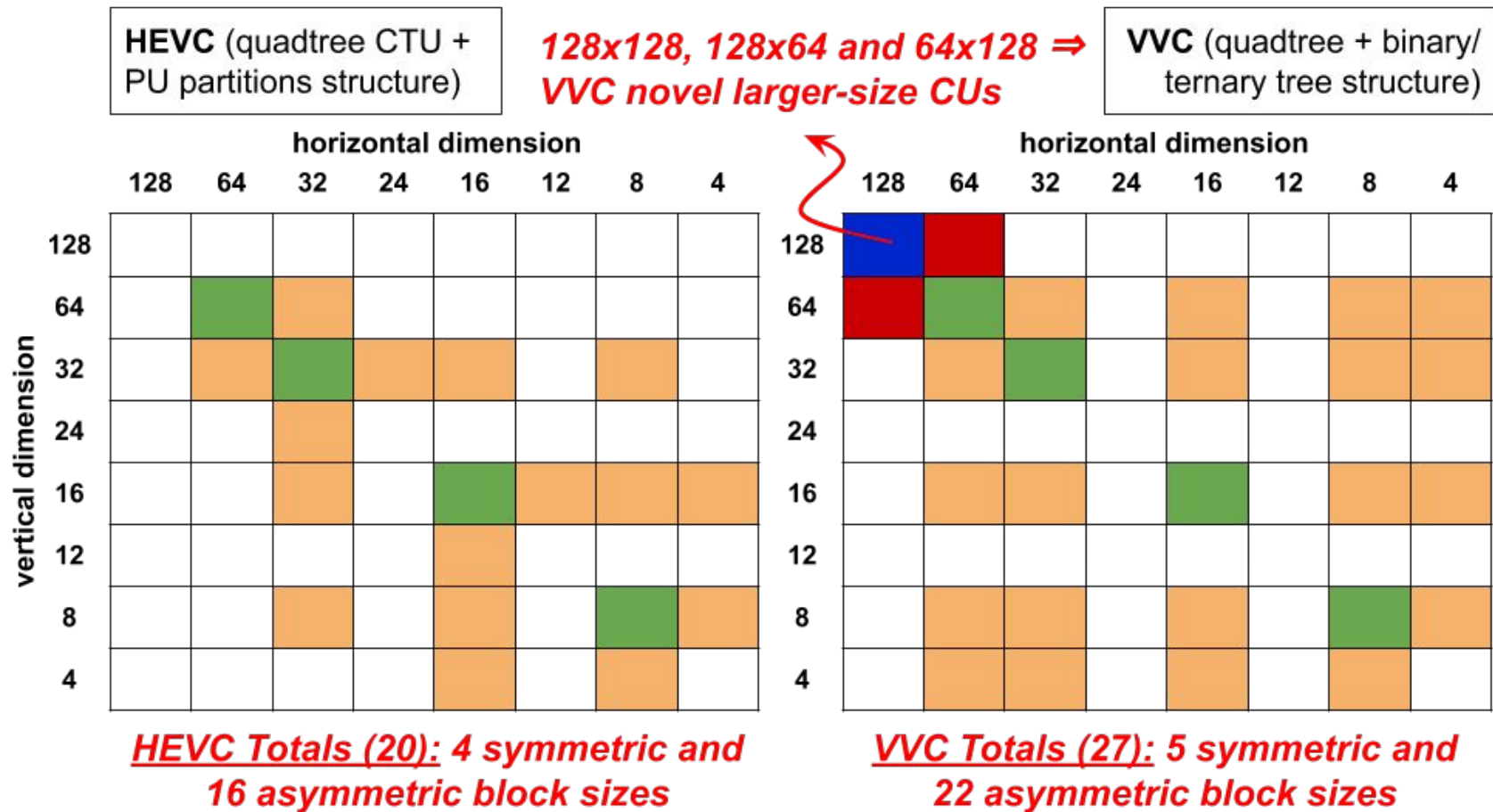
VVC Coding Structures

- VVC-novel block sizes



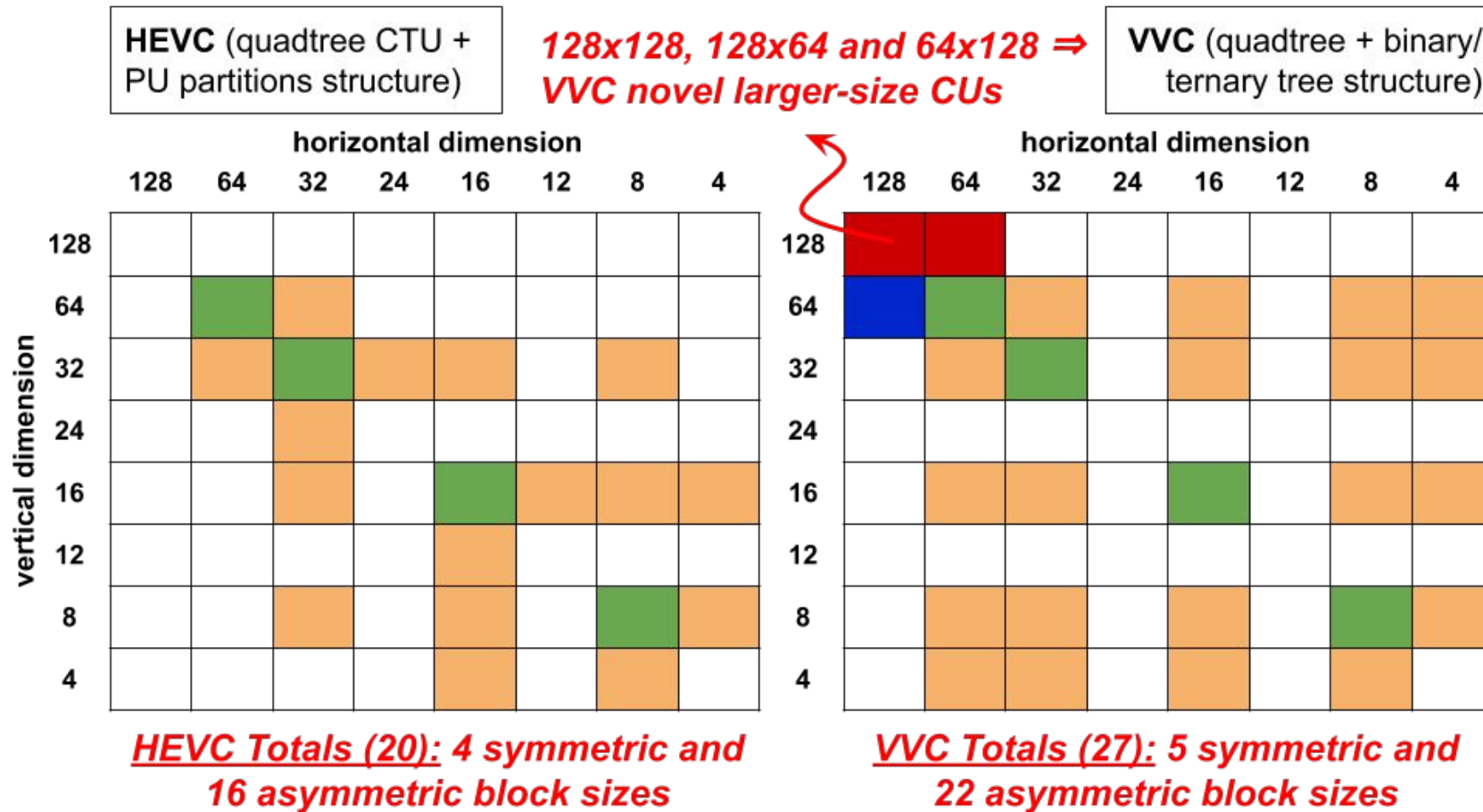
VVC Coding Structures

- VVC-novel block sizes



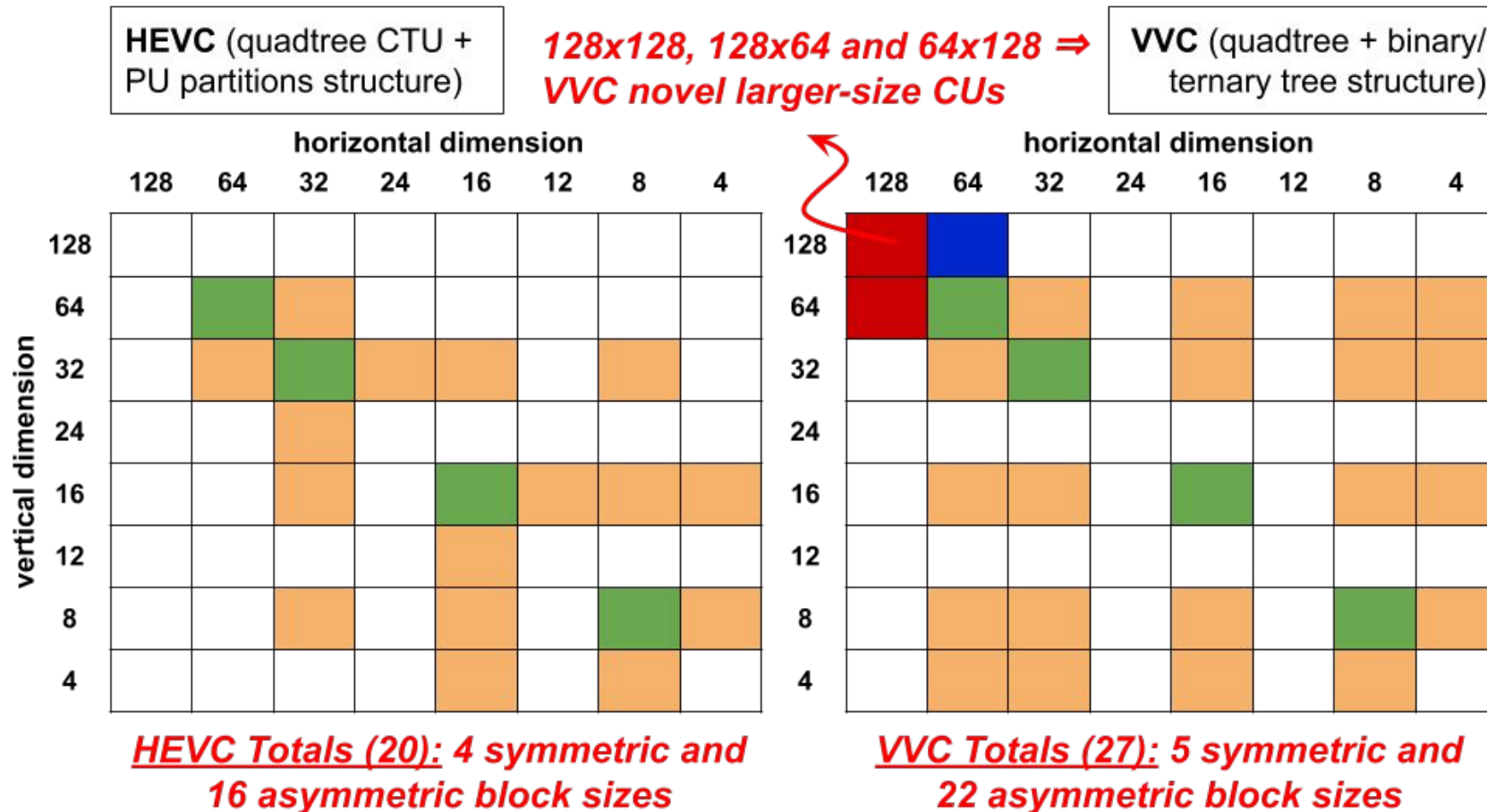
VVC Coding Structures

- VVC-novel block sizes



VVC Coding Structures

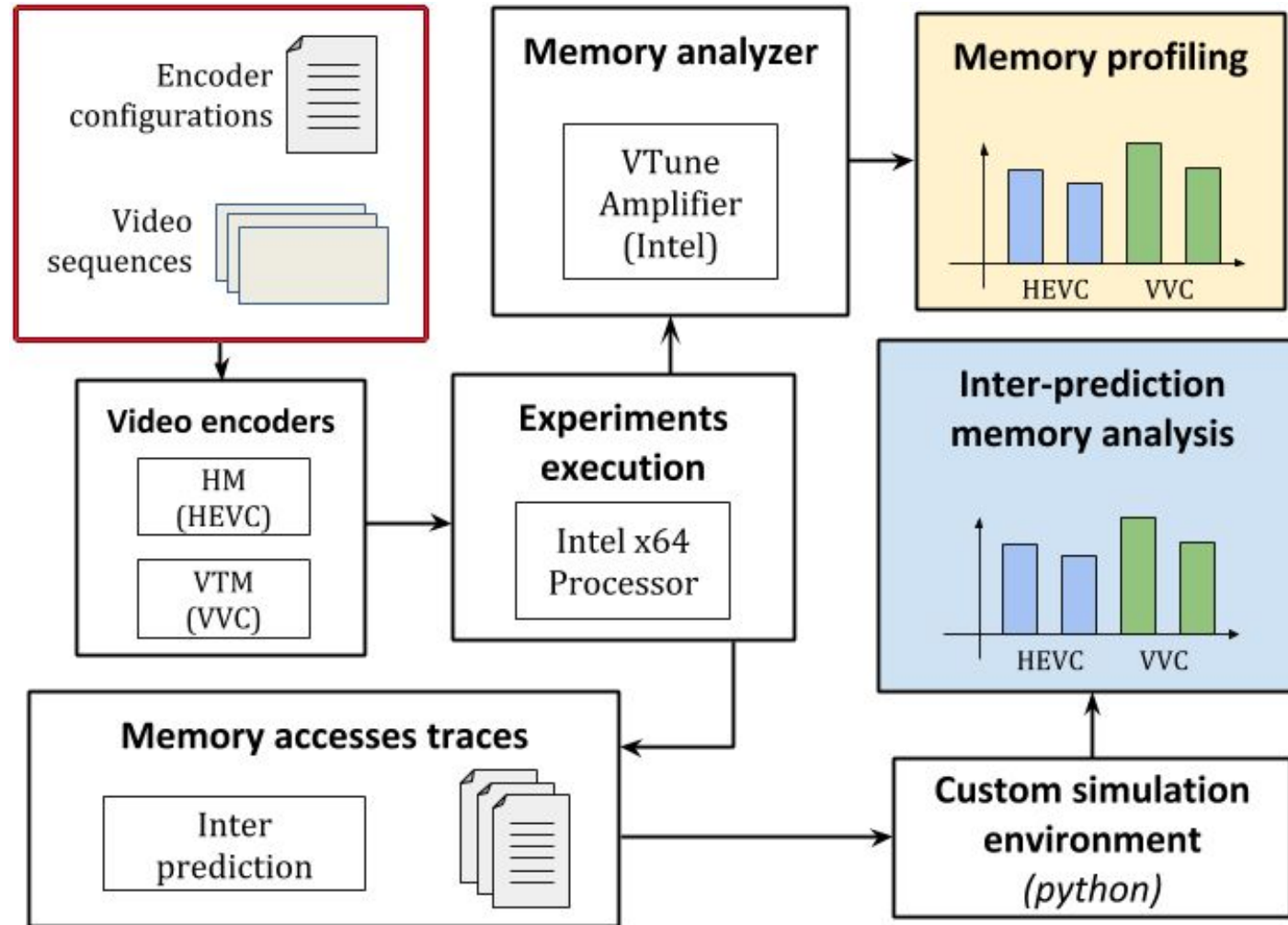
- VVC-novel block sizes



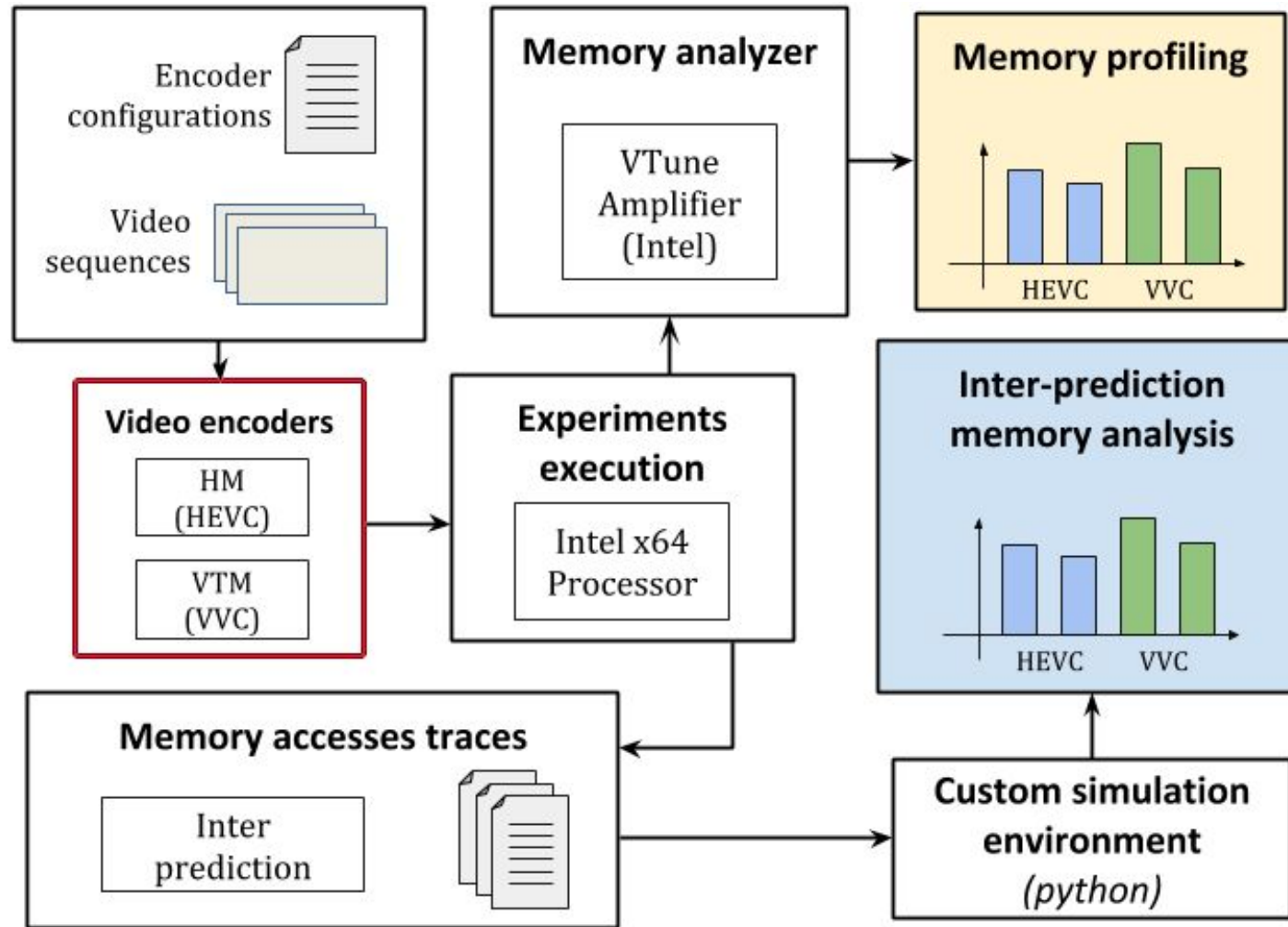
VVC Coding Structures

- VVC-novel block sizes
 - *The use of these large CU sizes enables better compression efficiency;*
 - *However, they also increases the encoder computational complexity.*

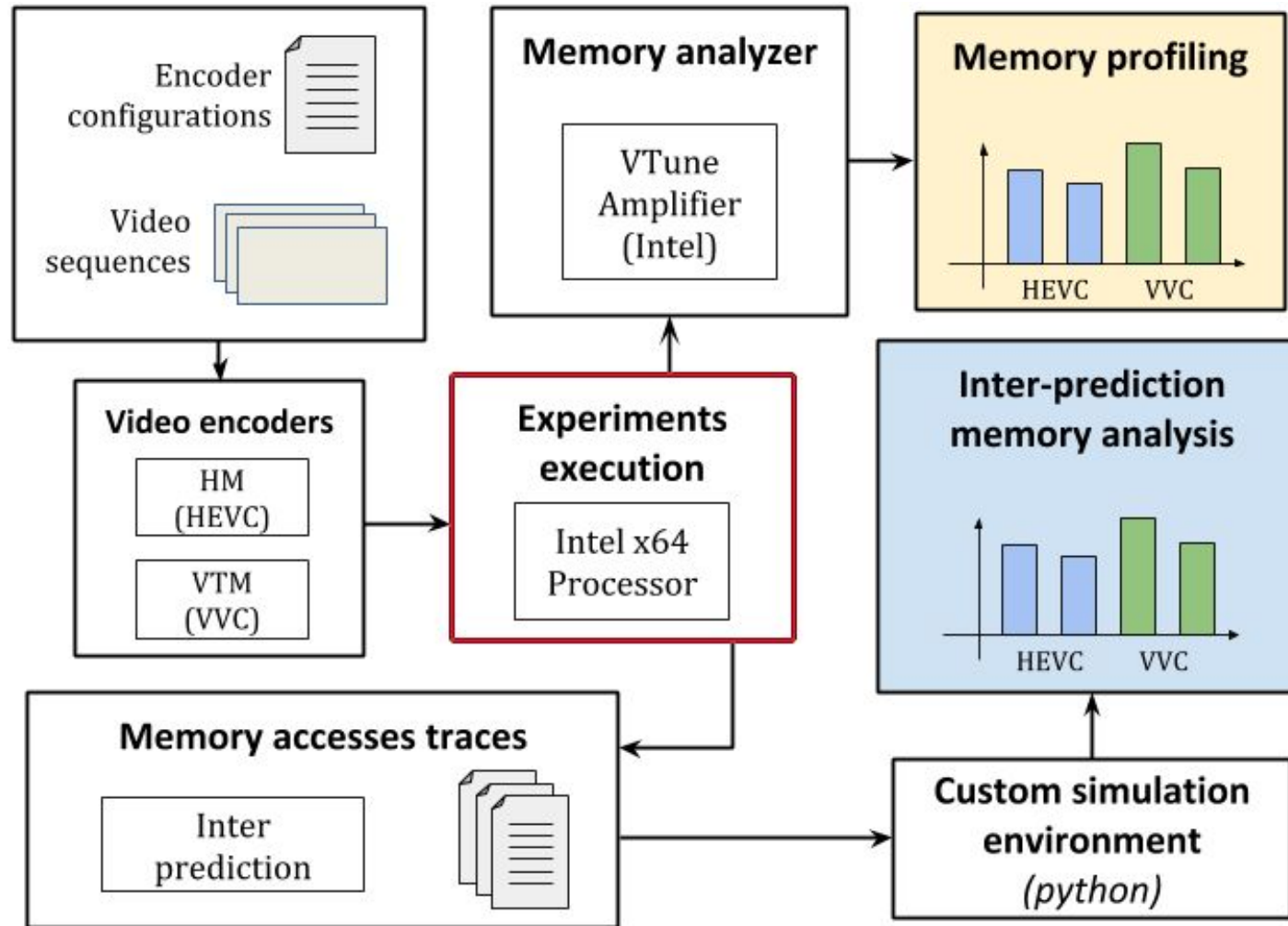
Experimental Setup



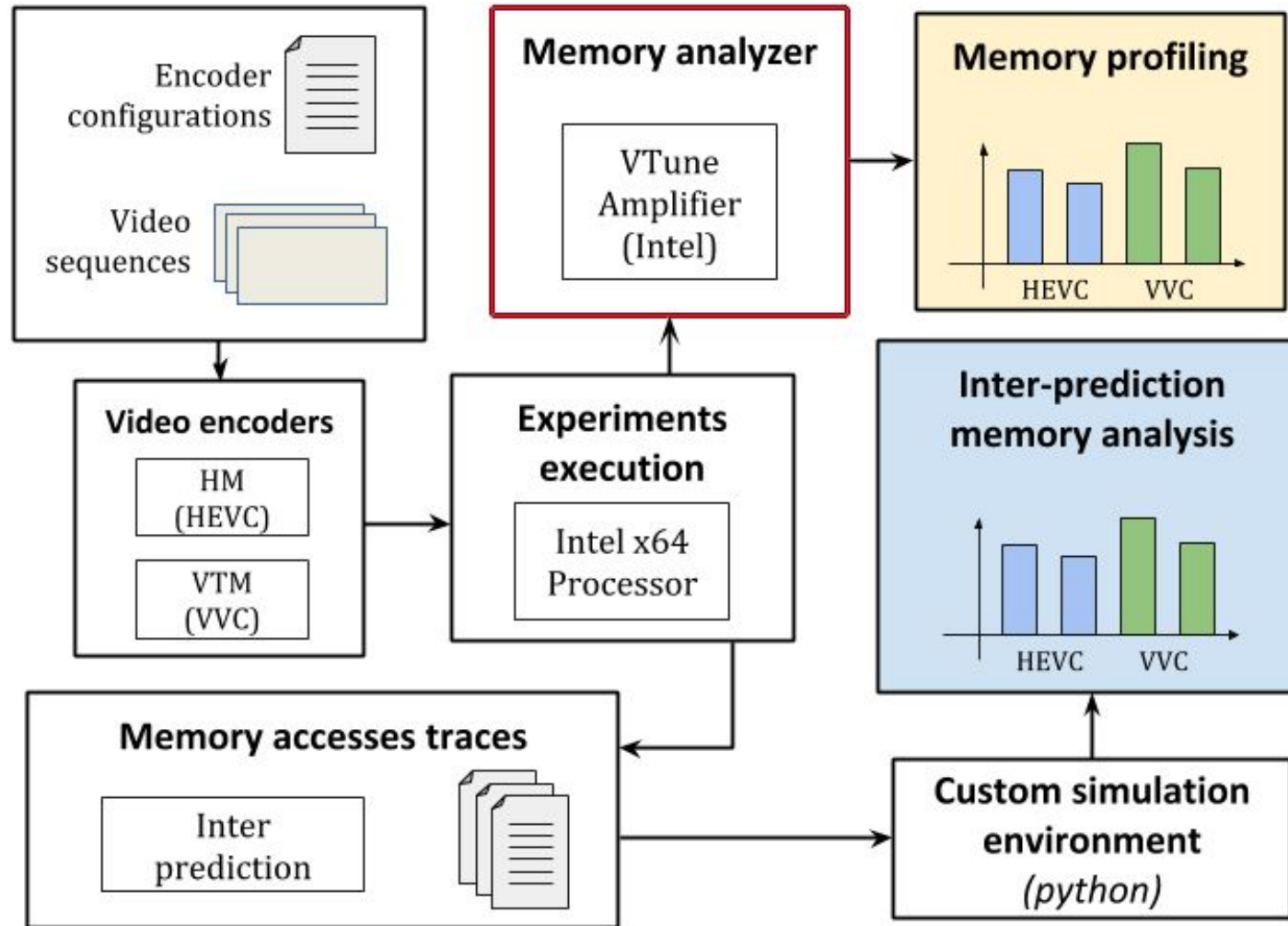
Experimental Setup



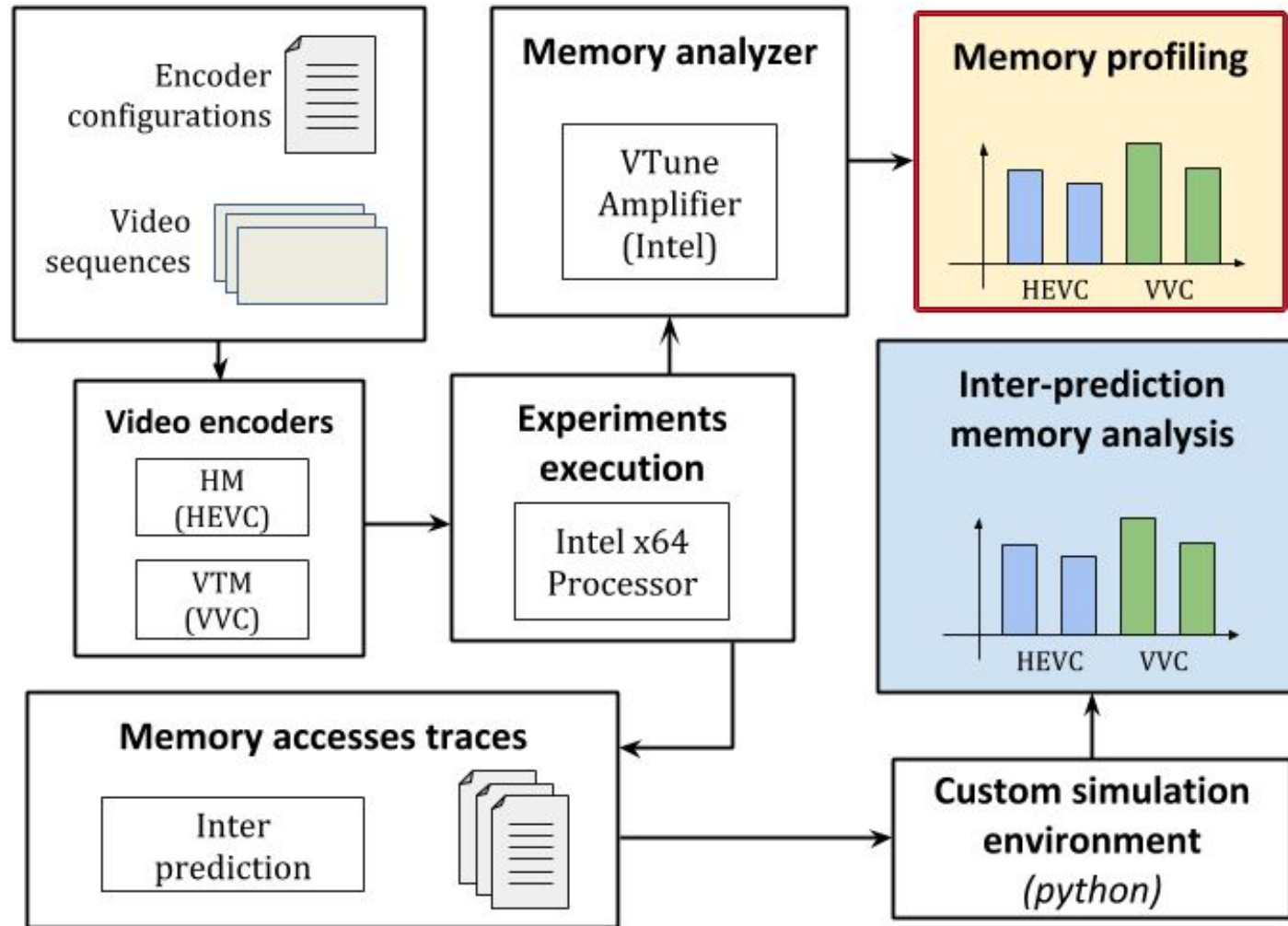
Experimental Setup



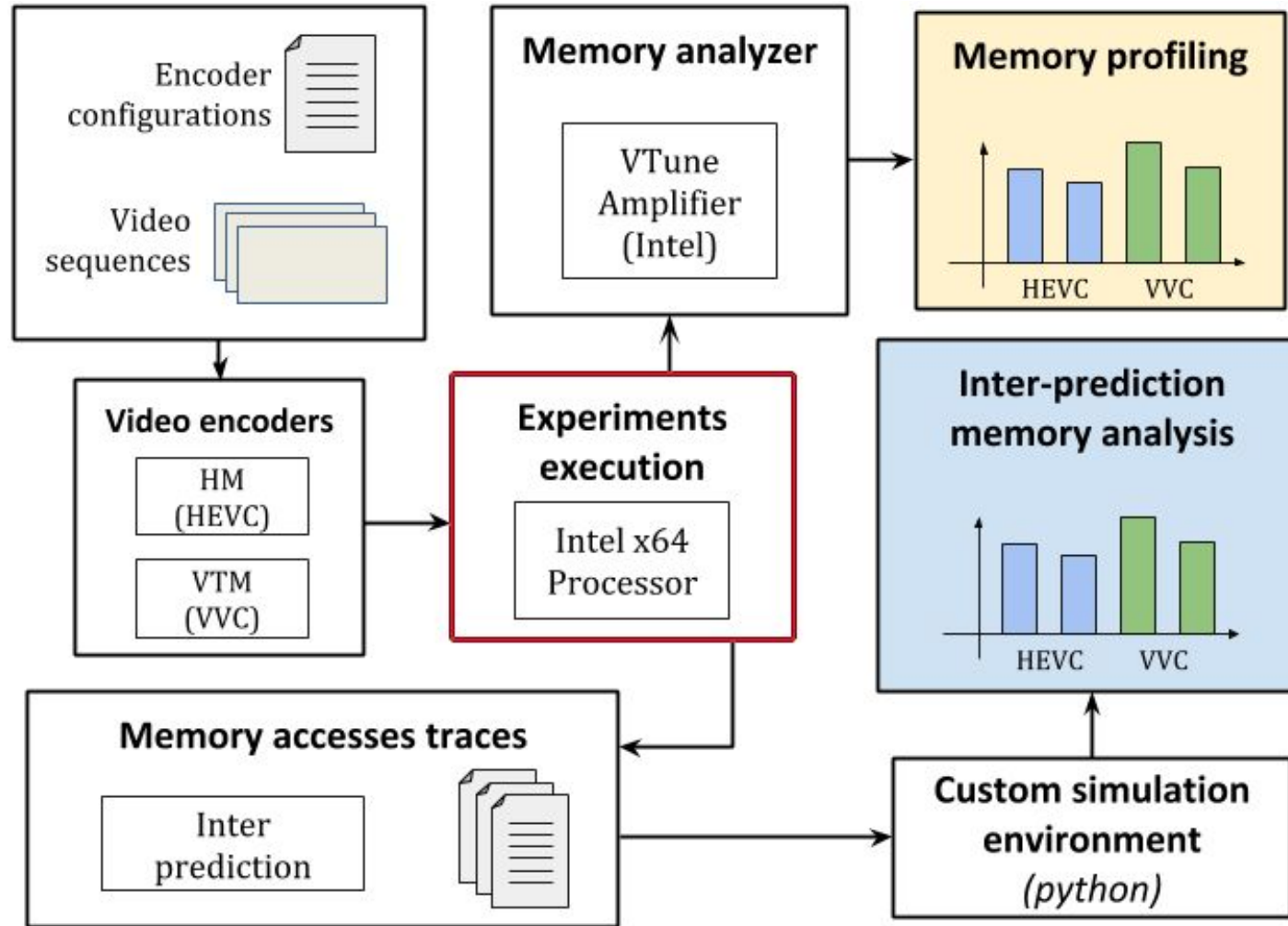
Experimental Setup



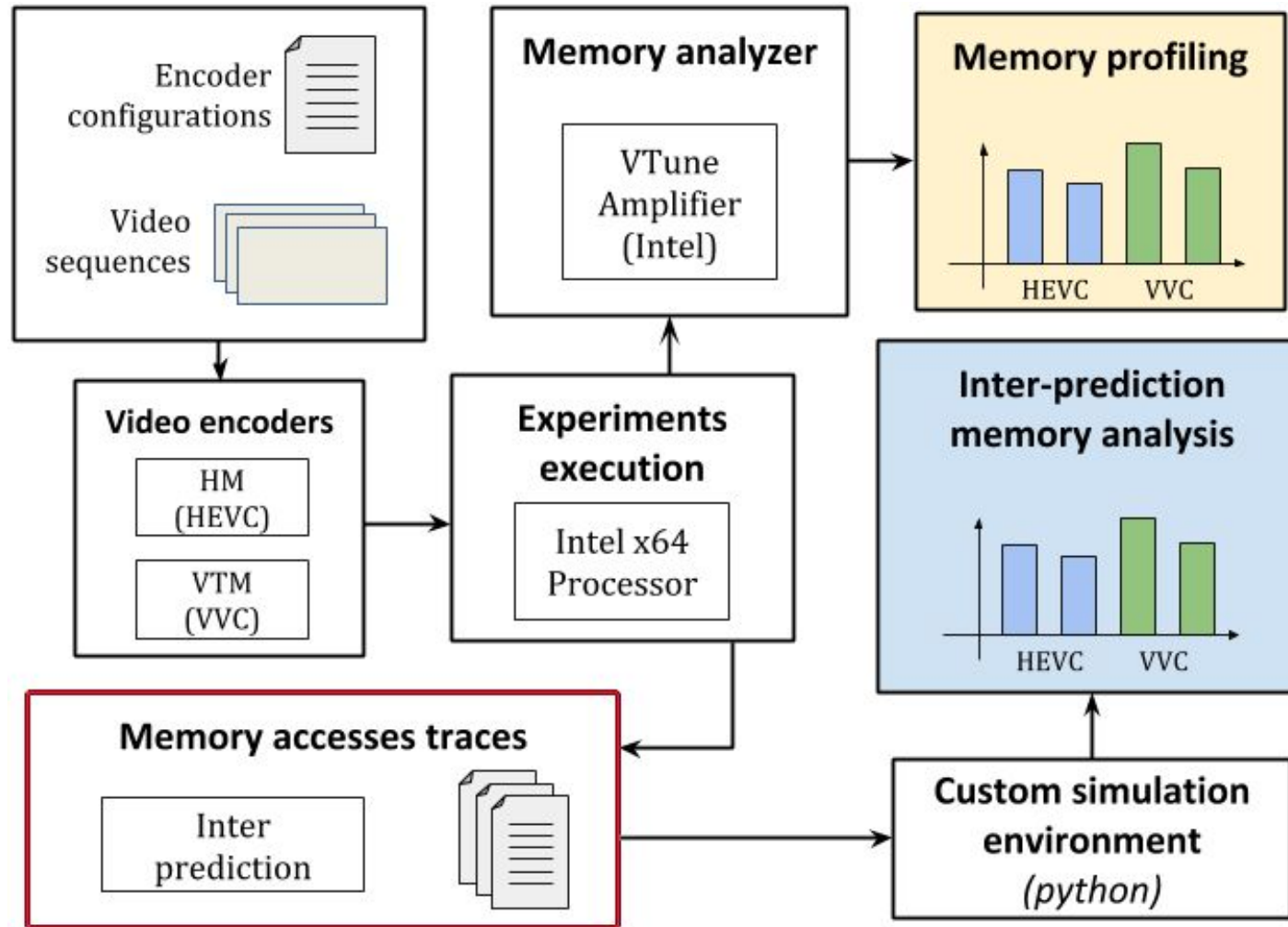
Experimental Setup



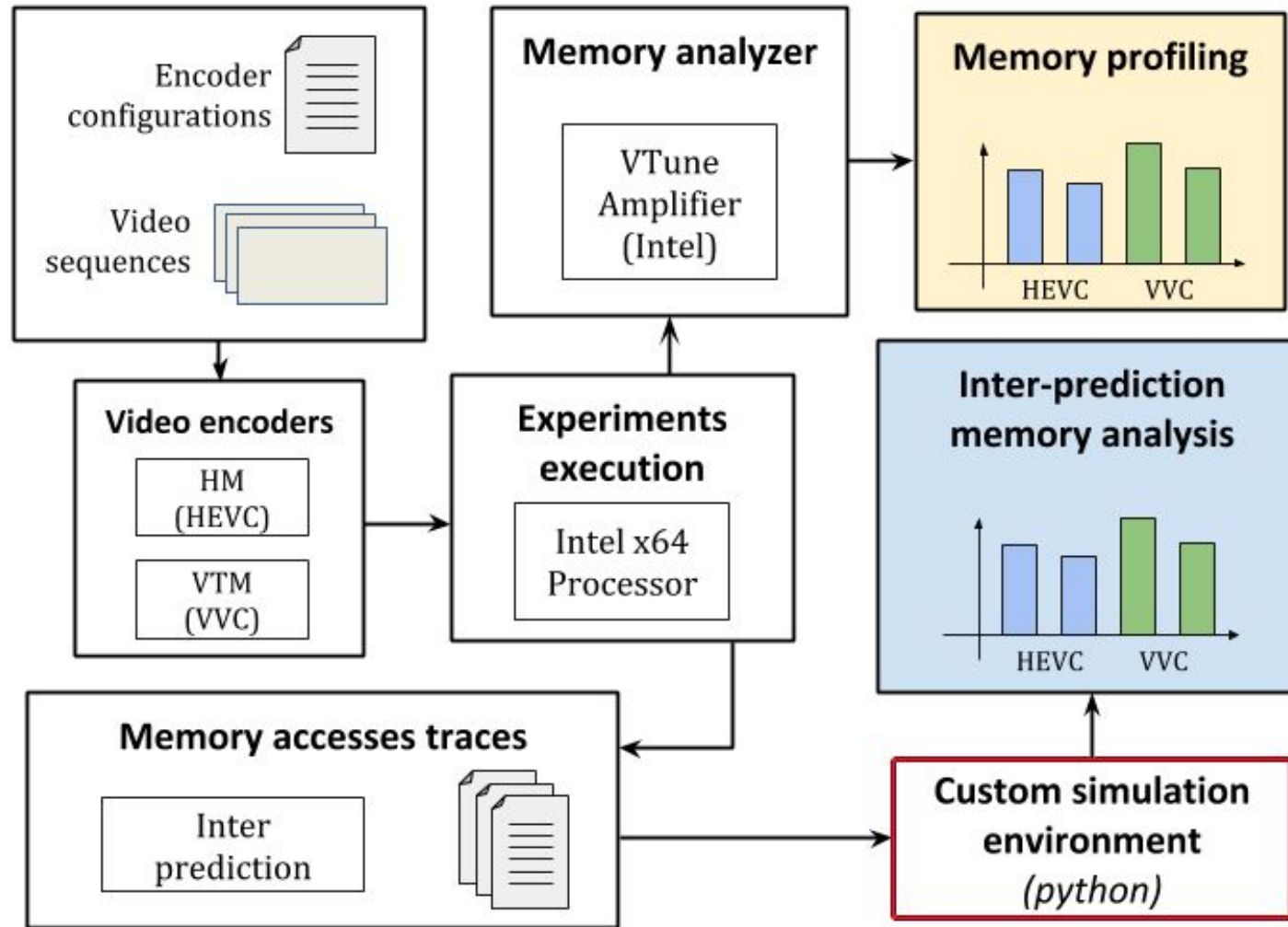
Experimental Setup



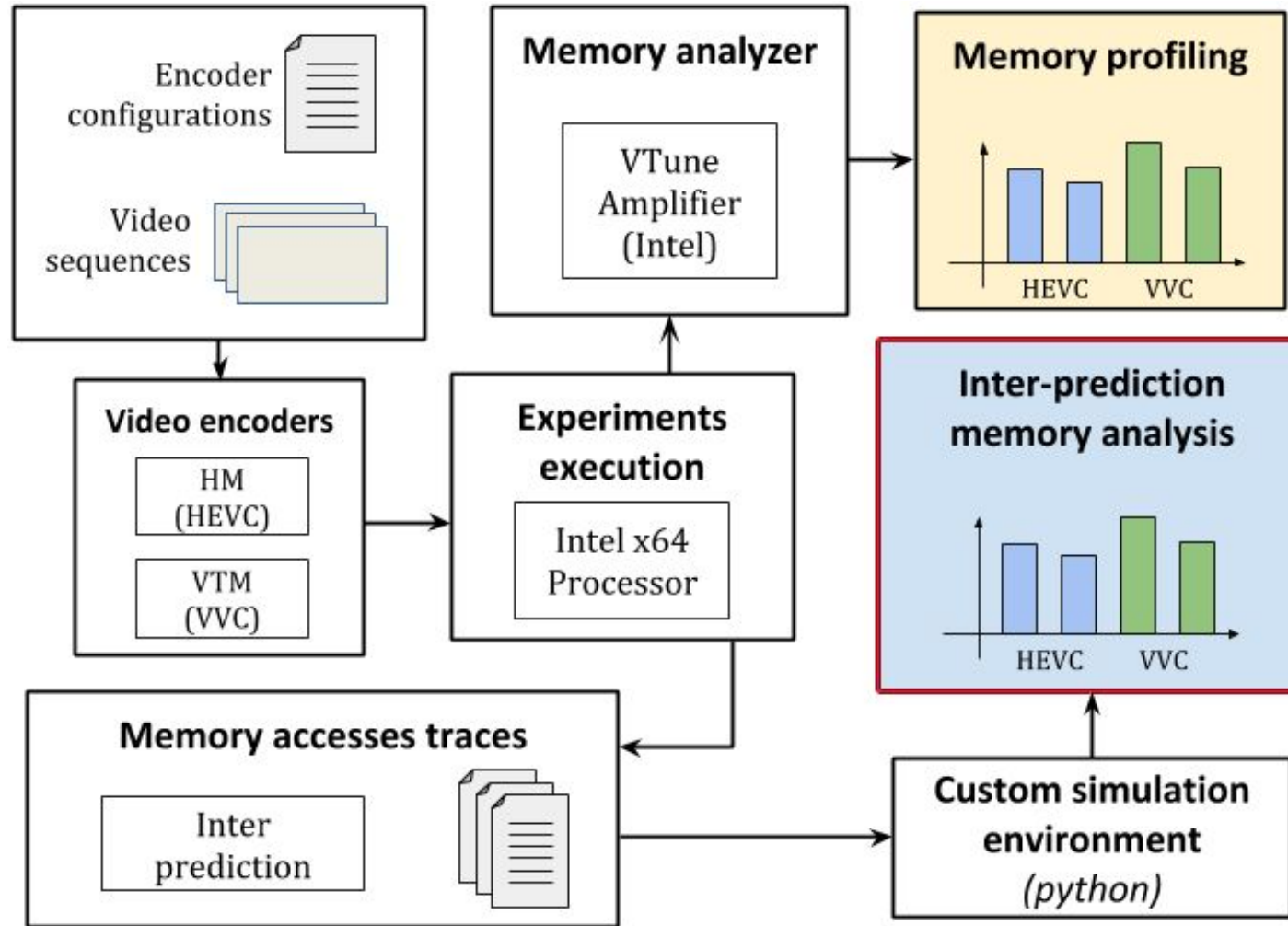
Experimental Setup



Experimental Setup

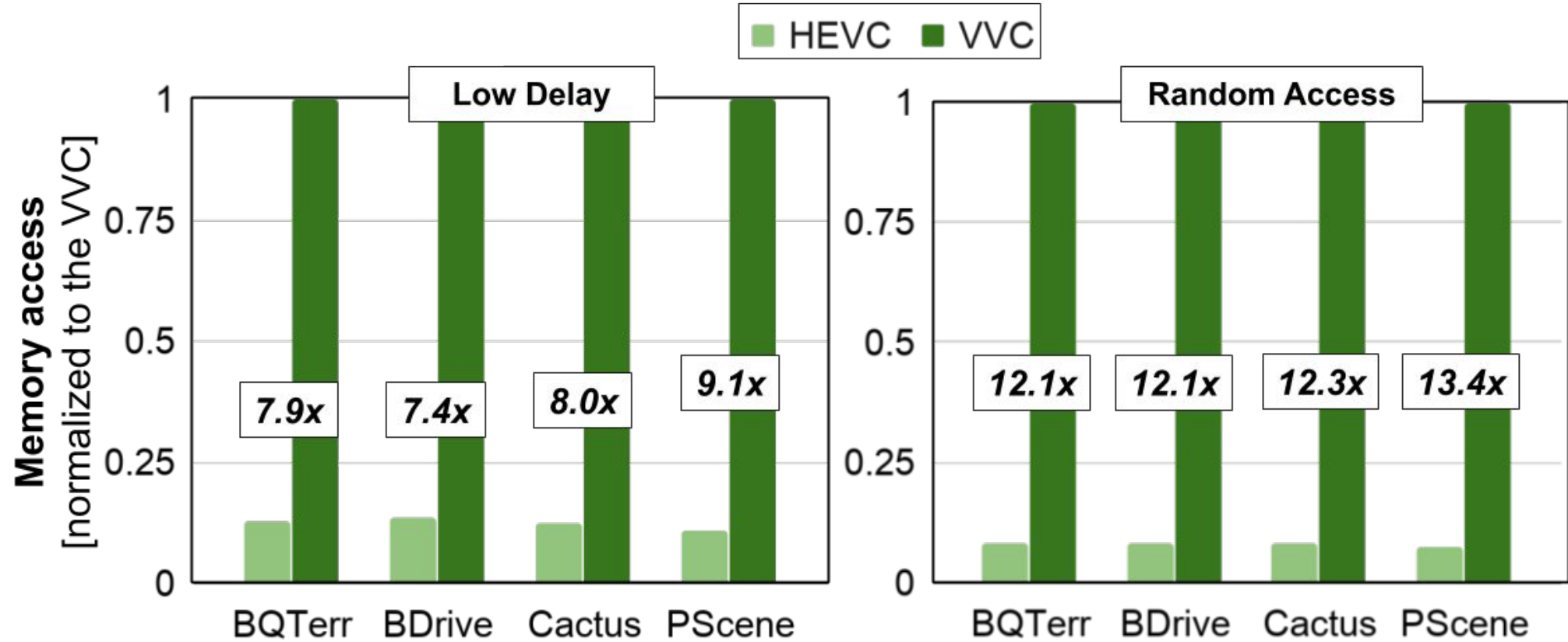


Experimental Setup



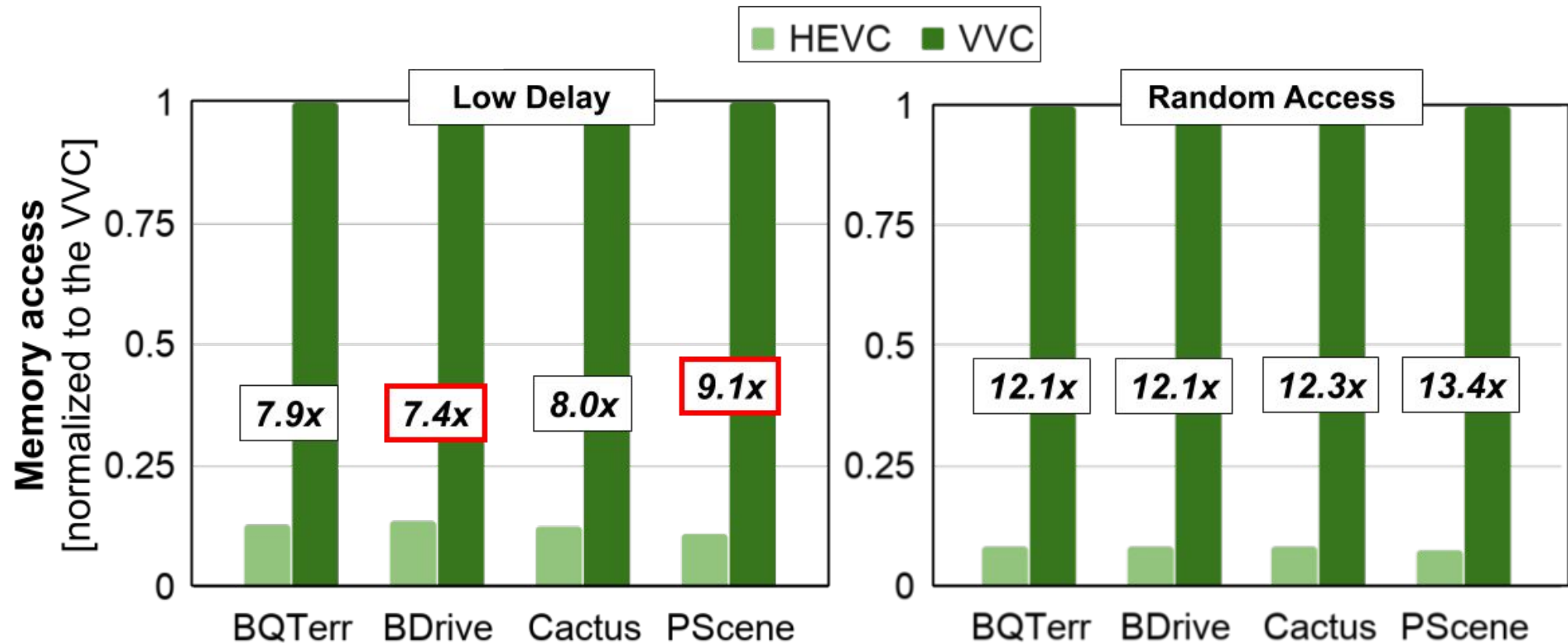
Memory Profiling

- Analysis-1



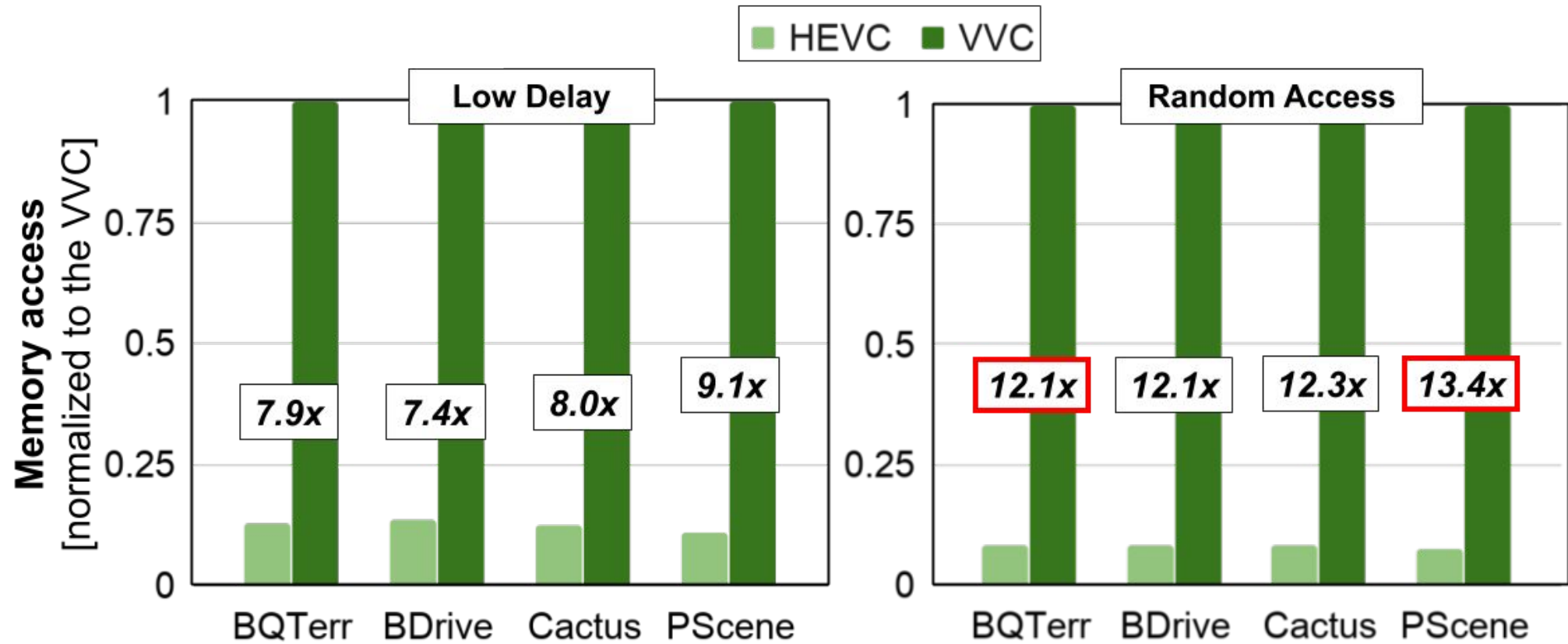
Memory Profiling

- Analysis-1



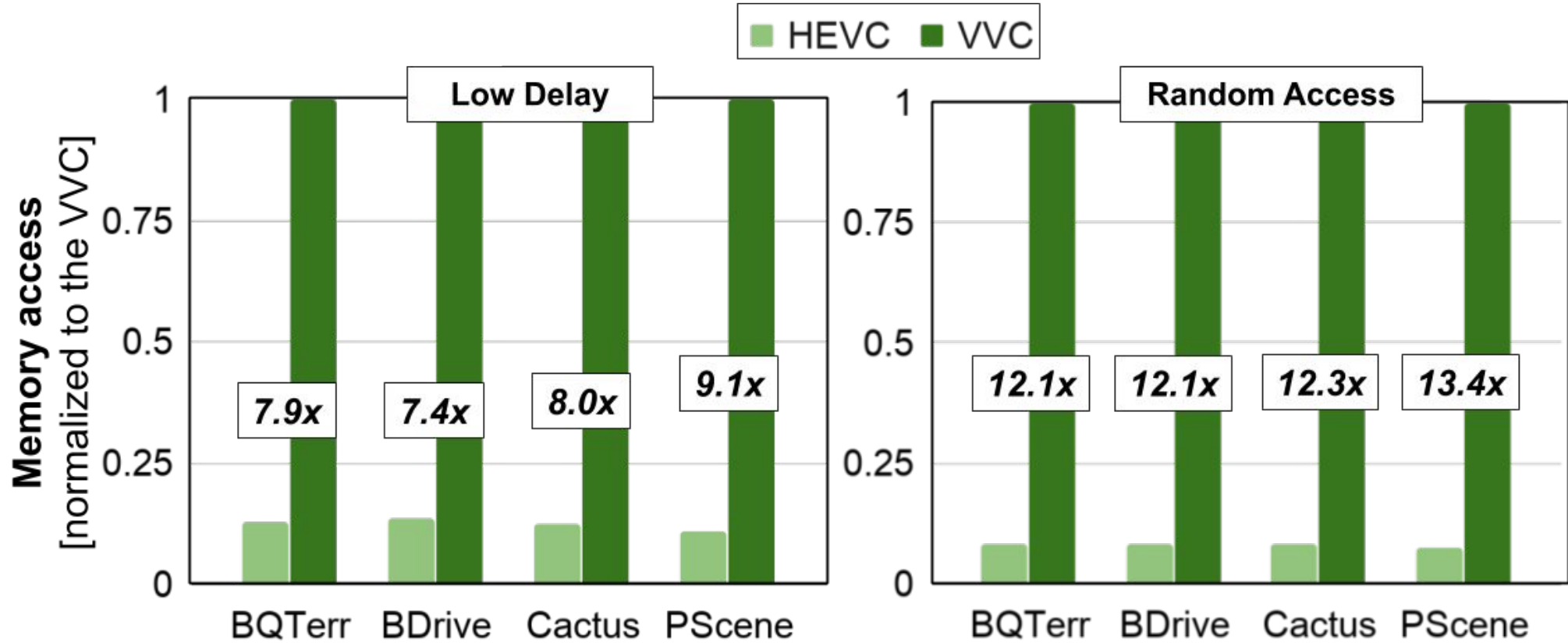
Memory Profiling

- Analysis-1



Memory Profiling

- Analysis-1

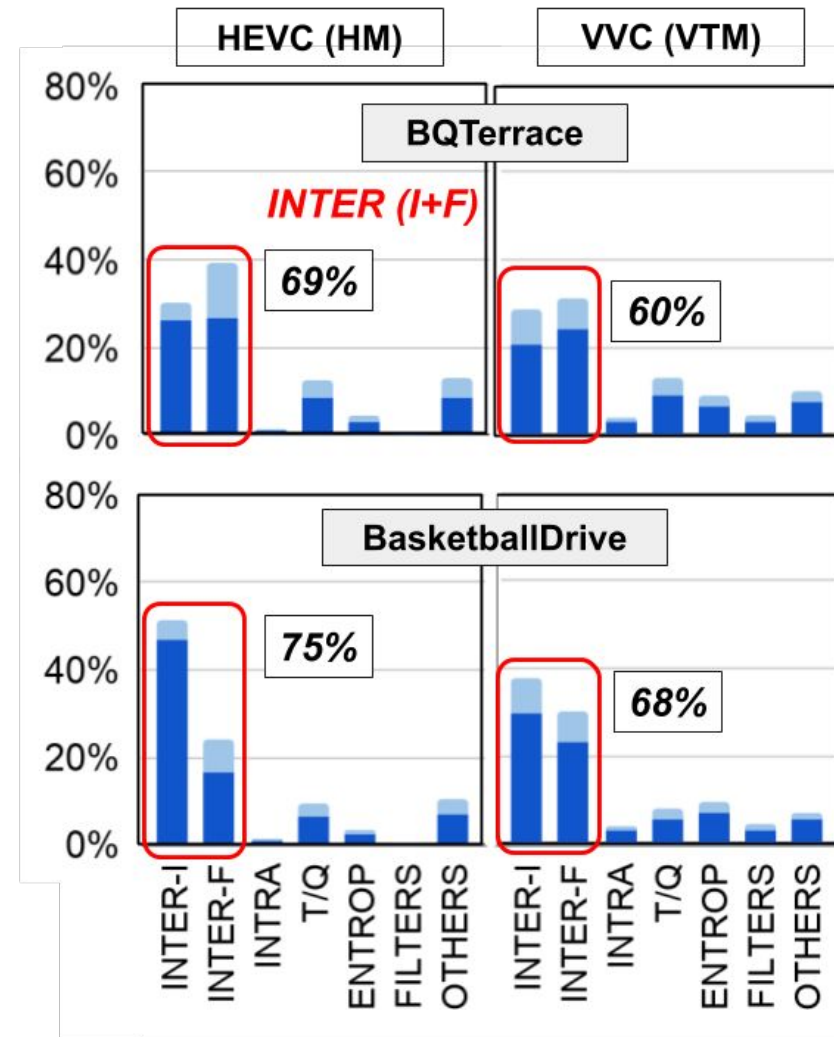


Memory Profiling

- Insights from Analysis-1
 - *The memory access increase of VTM shows the importance of evaluations in memory-related topics of VVC.*

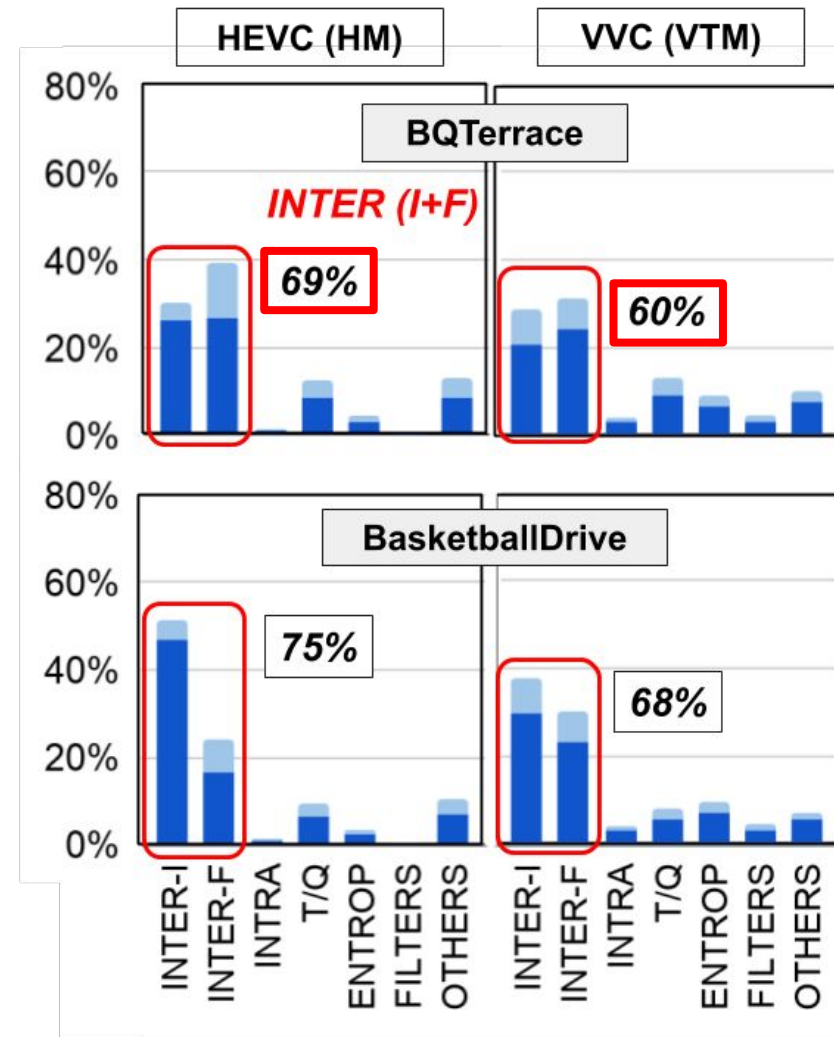
Memory Profiling

- Analysis-2



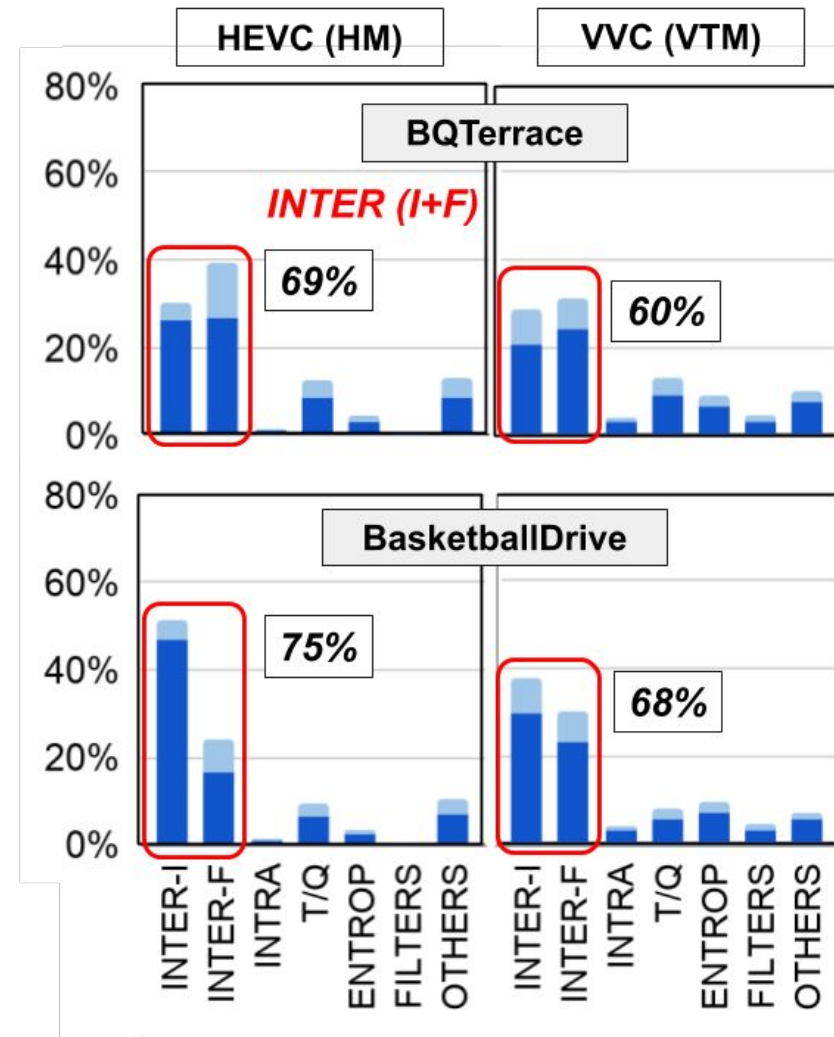
Memory Profiling

- Analysis-2



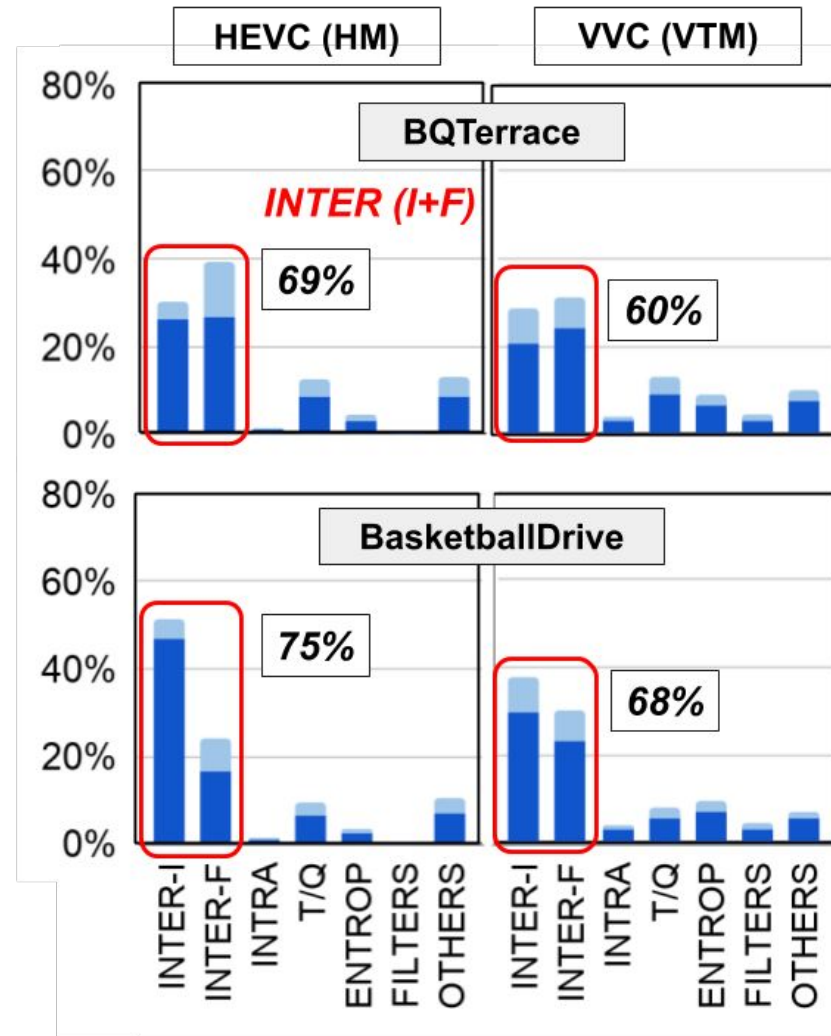
Memory Profiling

- Analysis-2



Memory Profiling

- Analysis-2

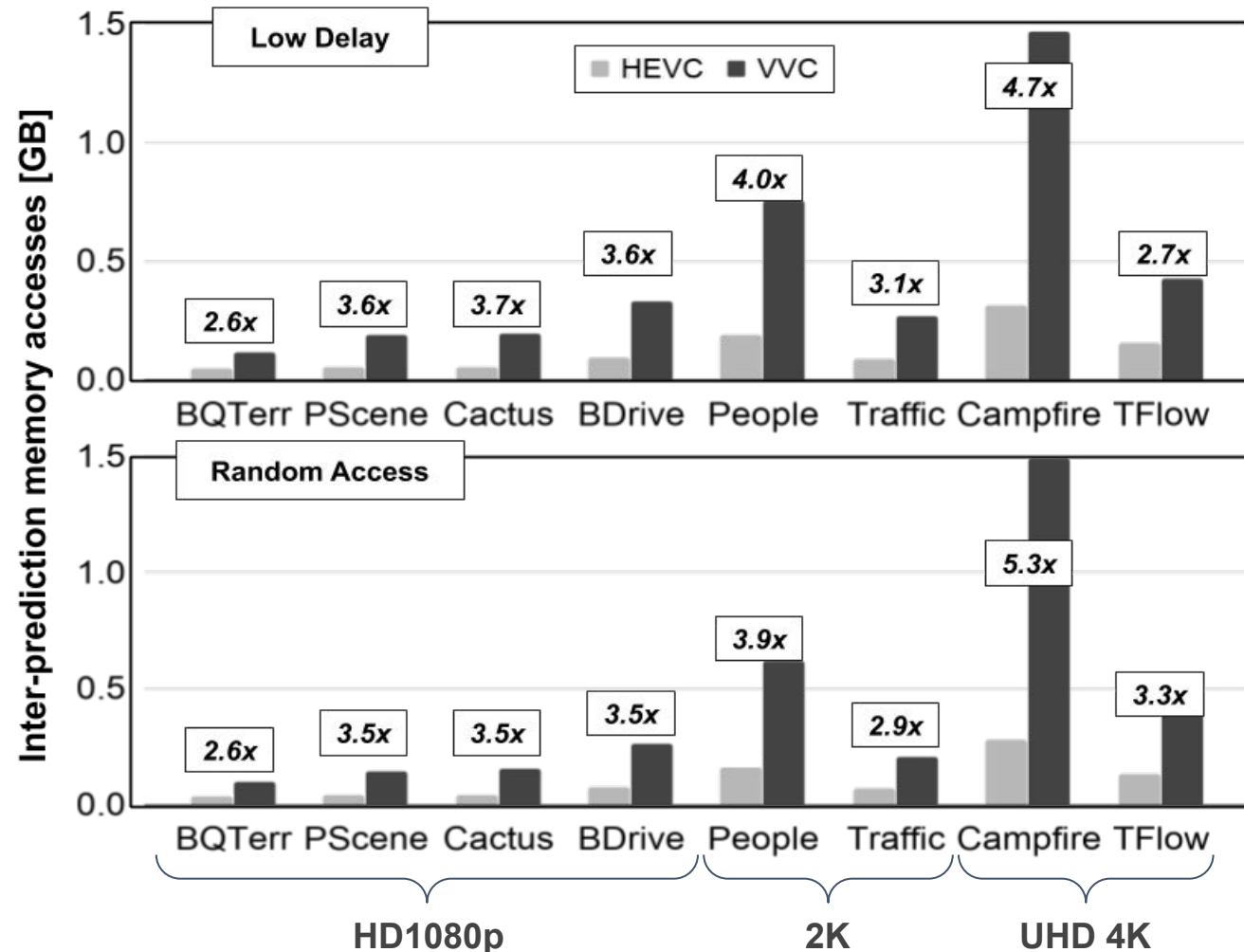


Memory Profiling

- Insights from Analysis-2
 - *Inter-frame prediction remains the most critical bottleneck at VVC;*
 - *Specific optimizations of this module are needed to enable memory-efficient VVC encoding.*

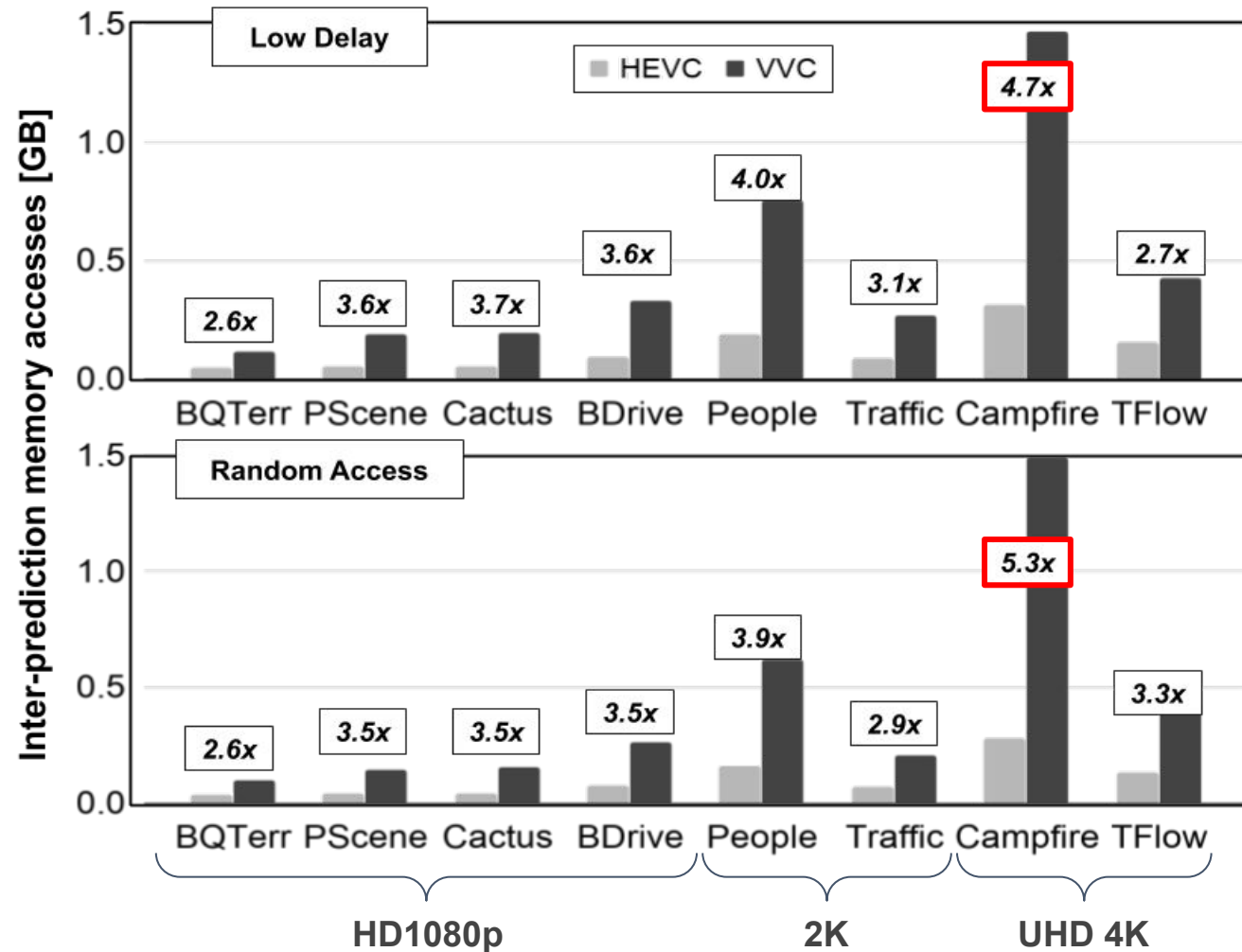
Inter Prediction Specific Memory Analysis

- Analysis-3



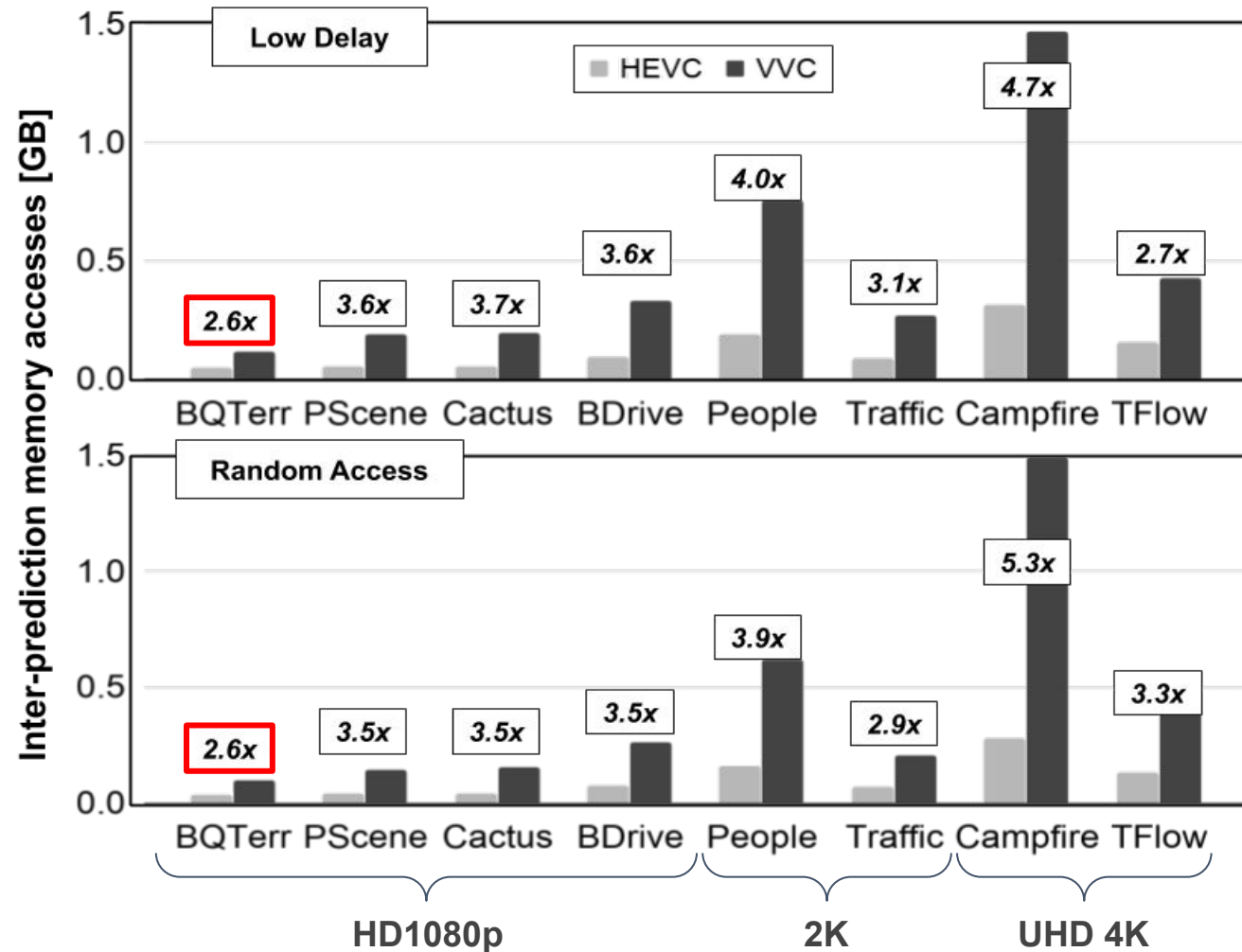
Inter Prediction Specific Memory Analysis

- Analysis-3



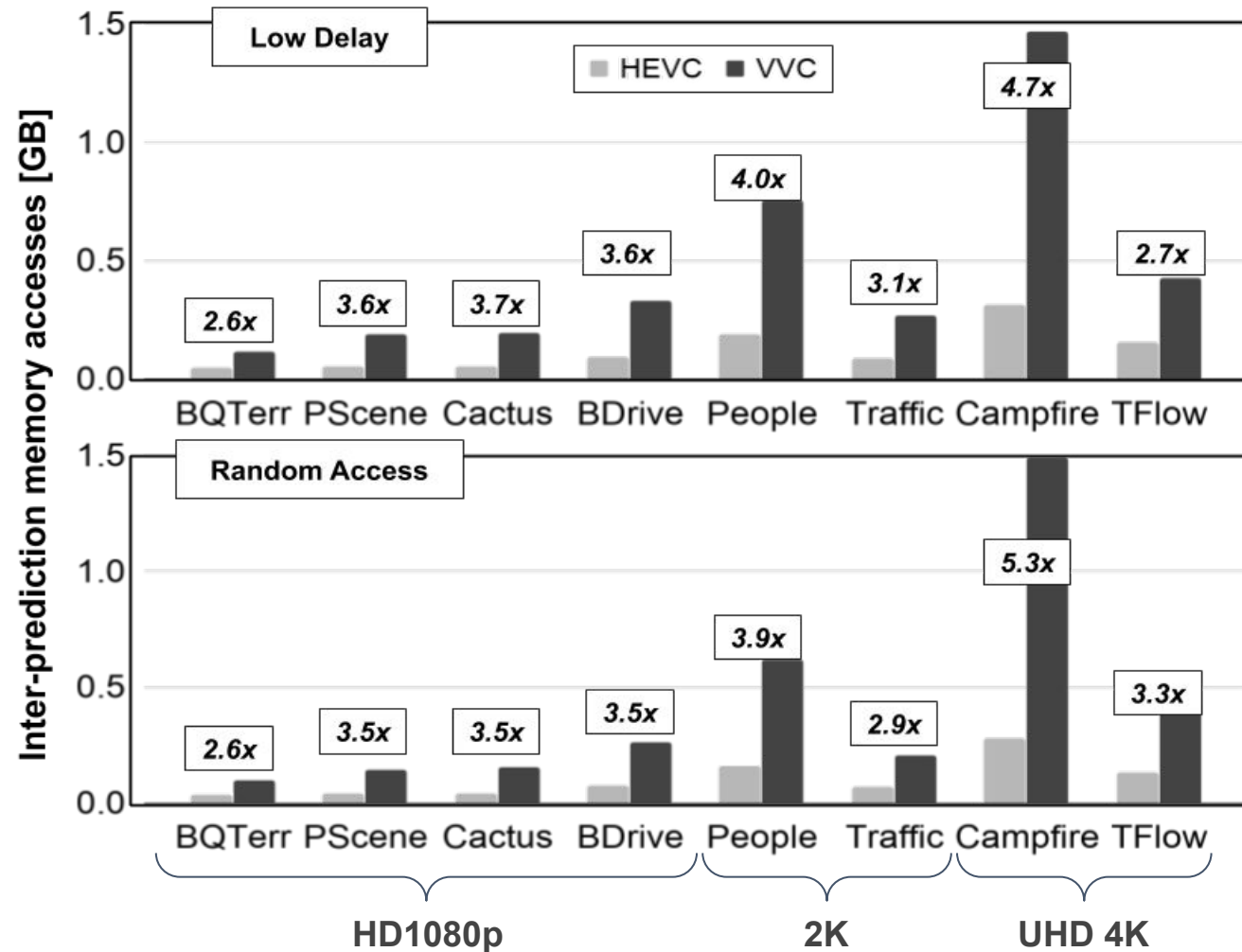
Inter Prediction Specific Memory Analysis

- Analysis-3



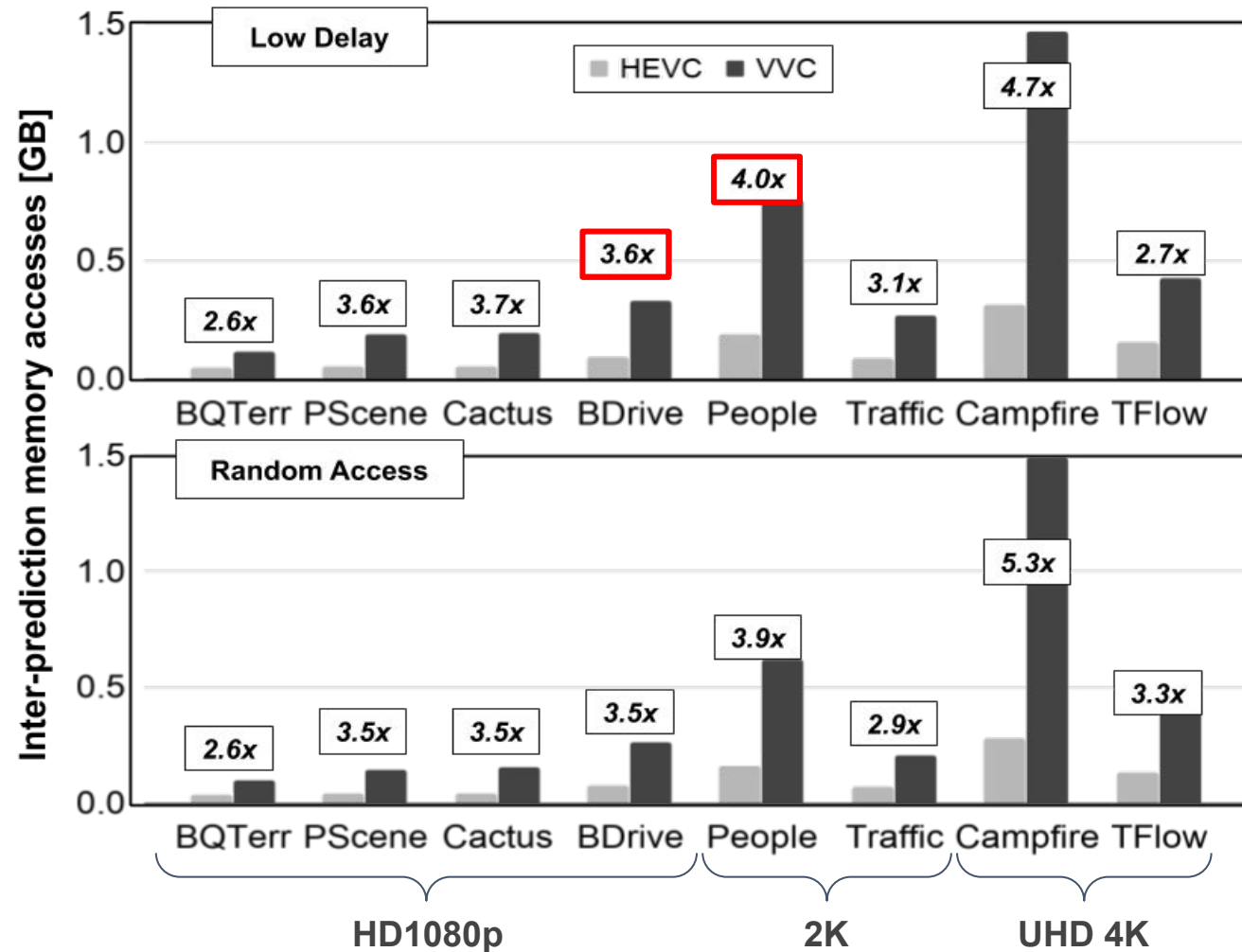
Inter Prediction Specific Memory Analysis

- Analysis-3



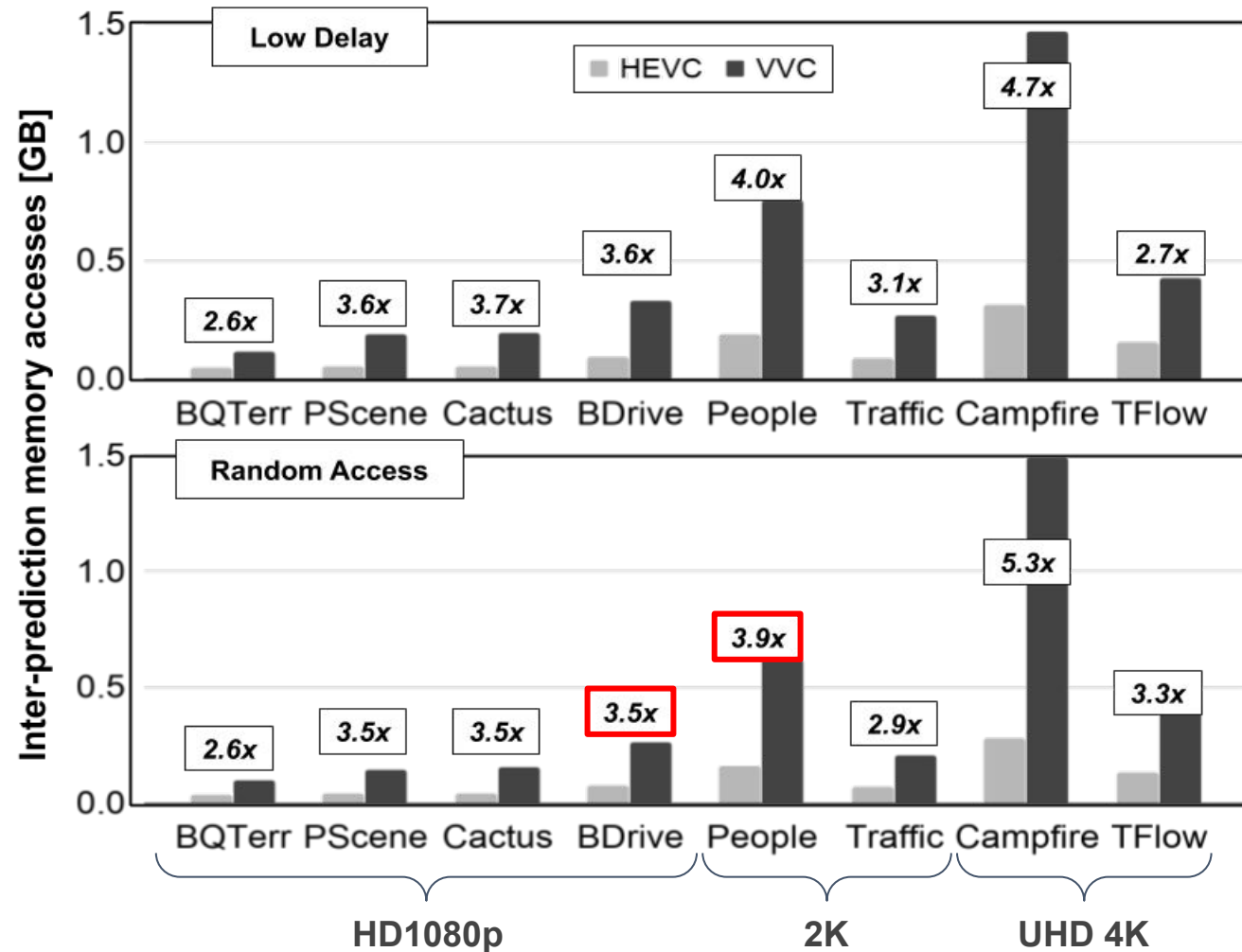
Inter Prediction Specific Memory Analysis

- Analysis-3



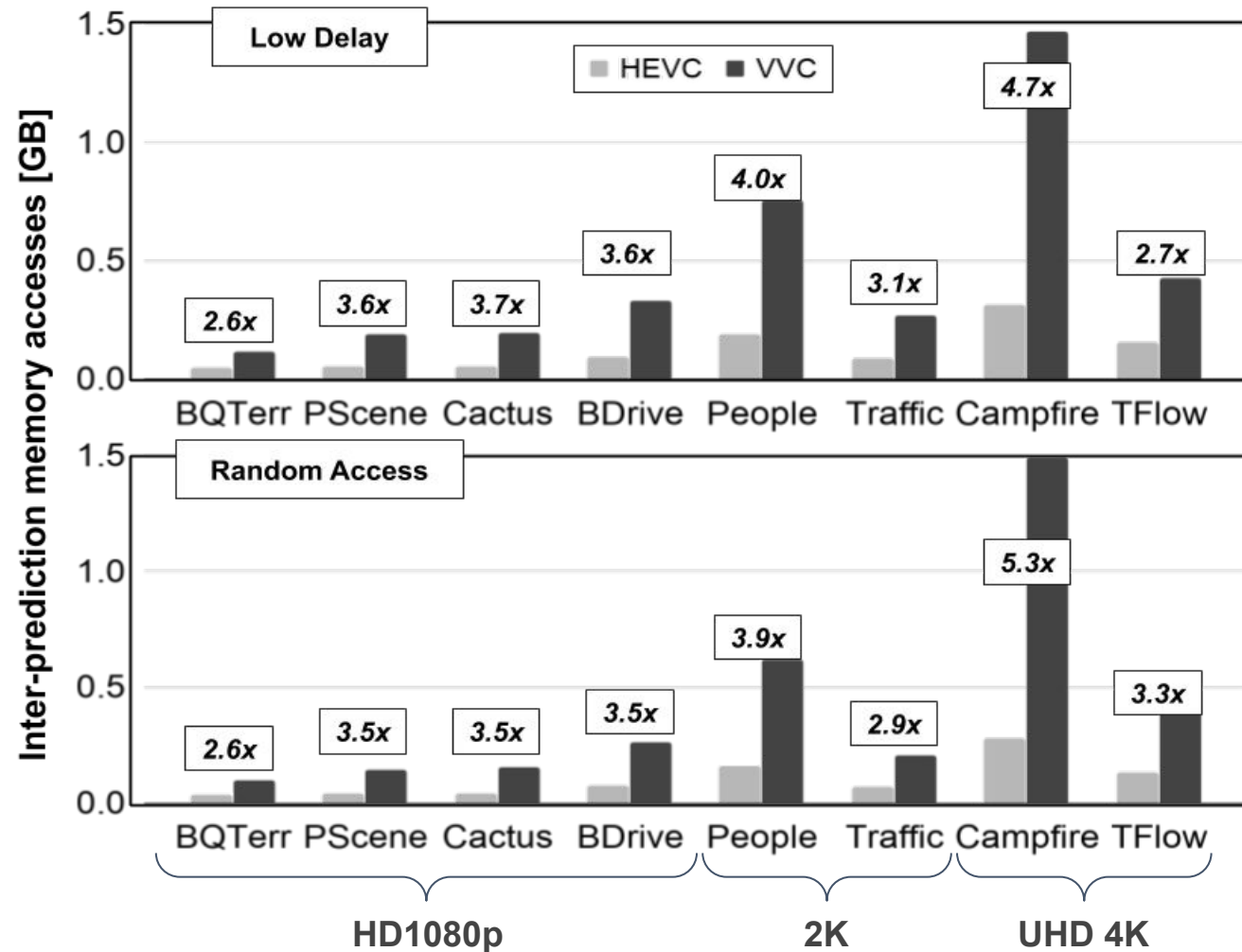
Inter Prediction Specific Memory Analysis

- Analysis-3



Inter Prediction Specific Memory Analysis

- Analysis-3

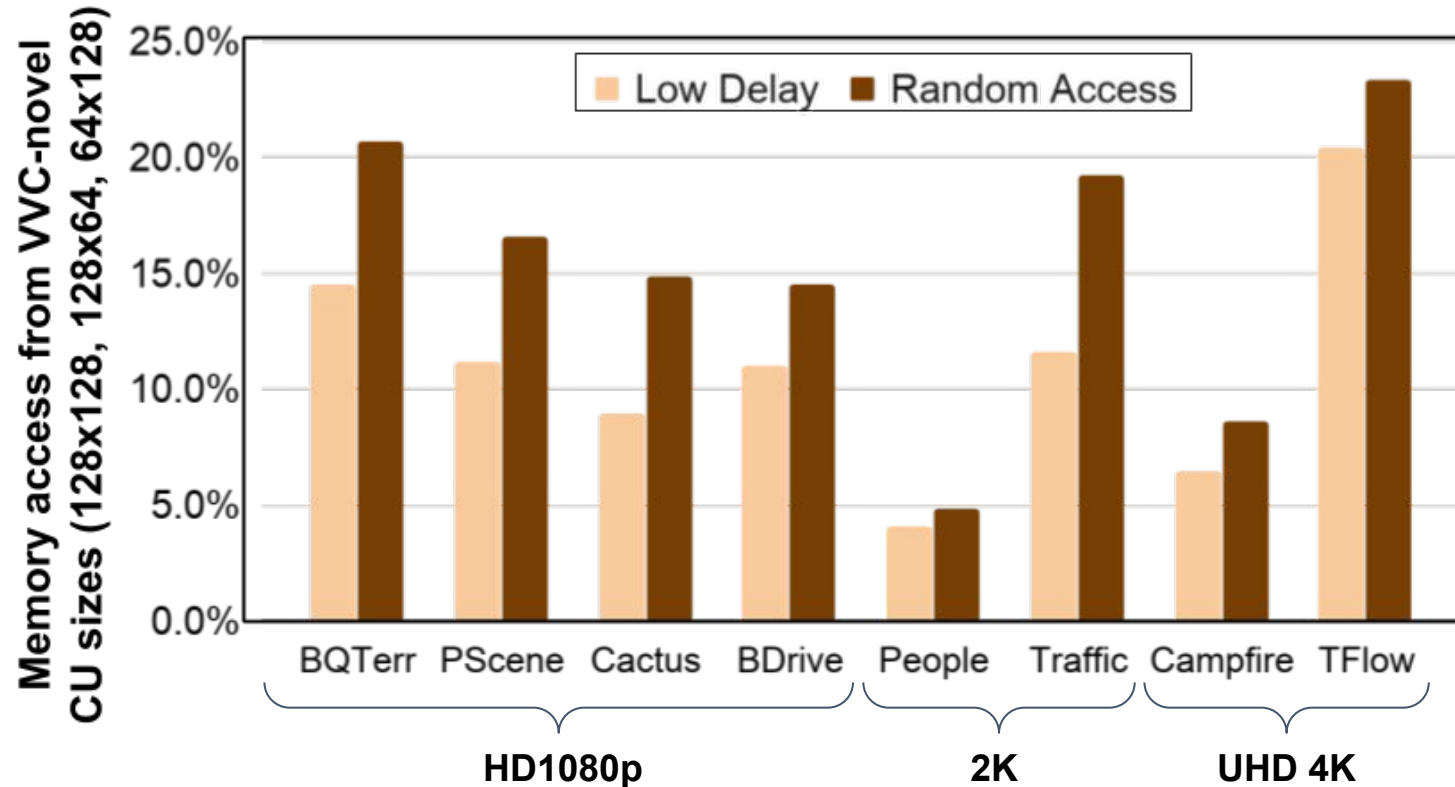


Memory Profiling

- Insights from Analysis-3
 - *The novelties of VVC lead to increased memory requirements for inter-prediction, which can reach 5.3x of overhead.*

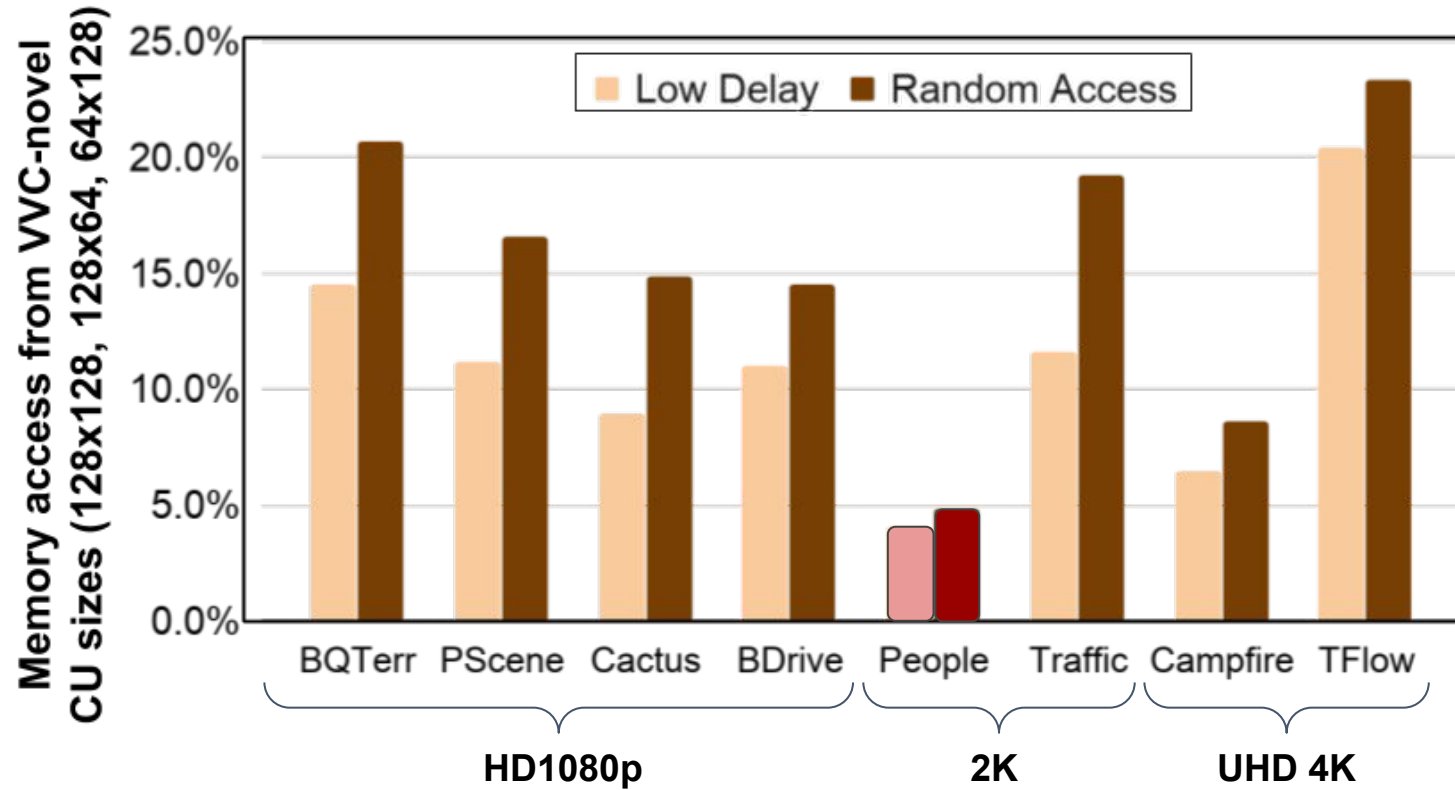
Inter Prediction Specific Memory Analysis

- Analysis-4



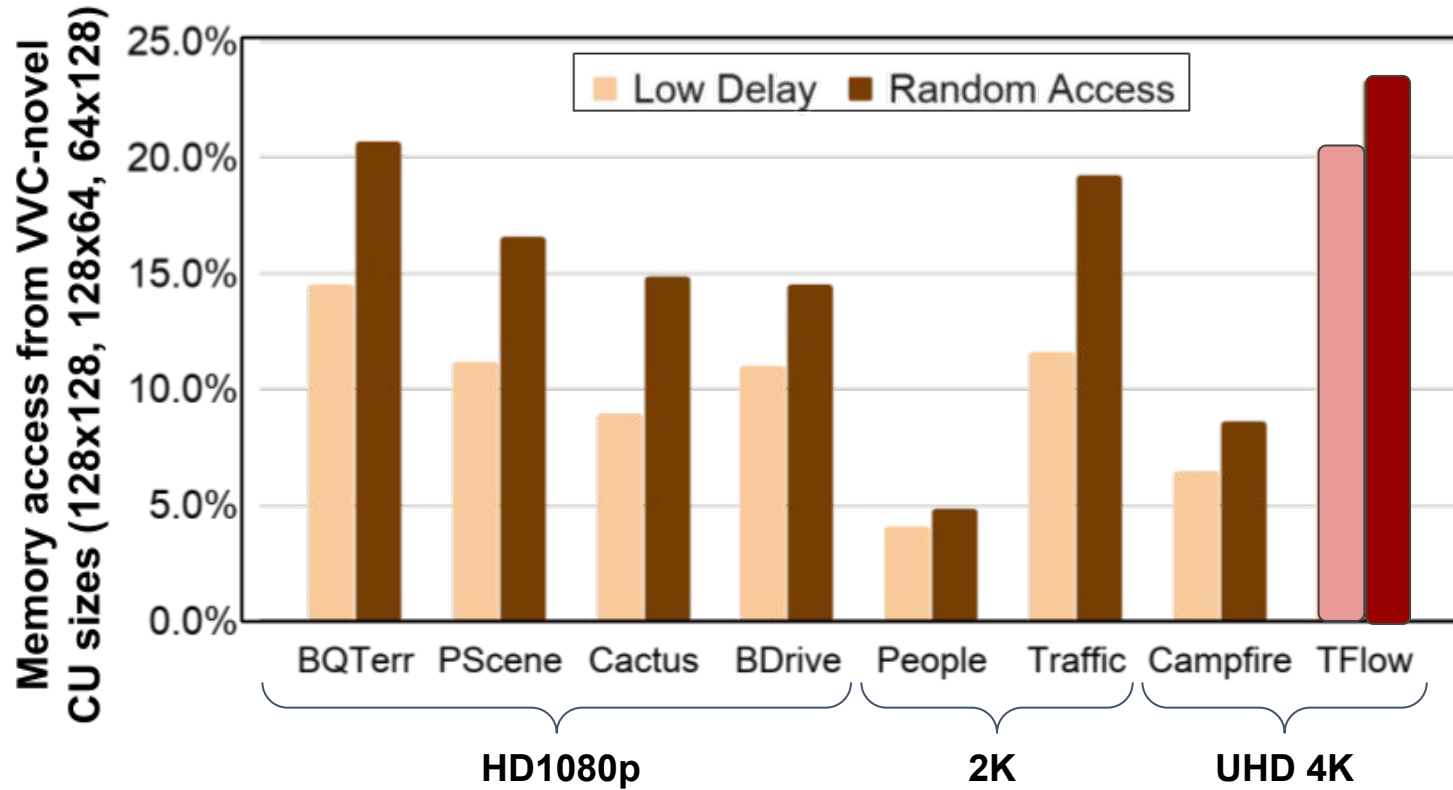
Inter Prediction Specific Memory Analysis

- Analysis-4



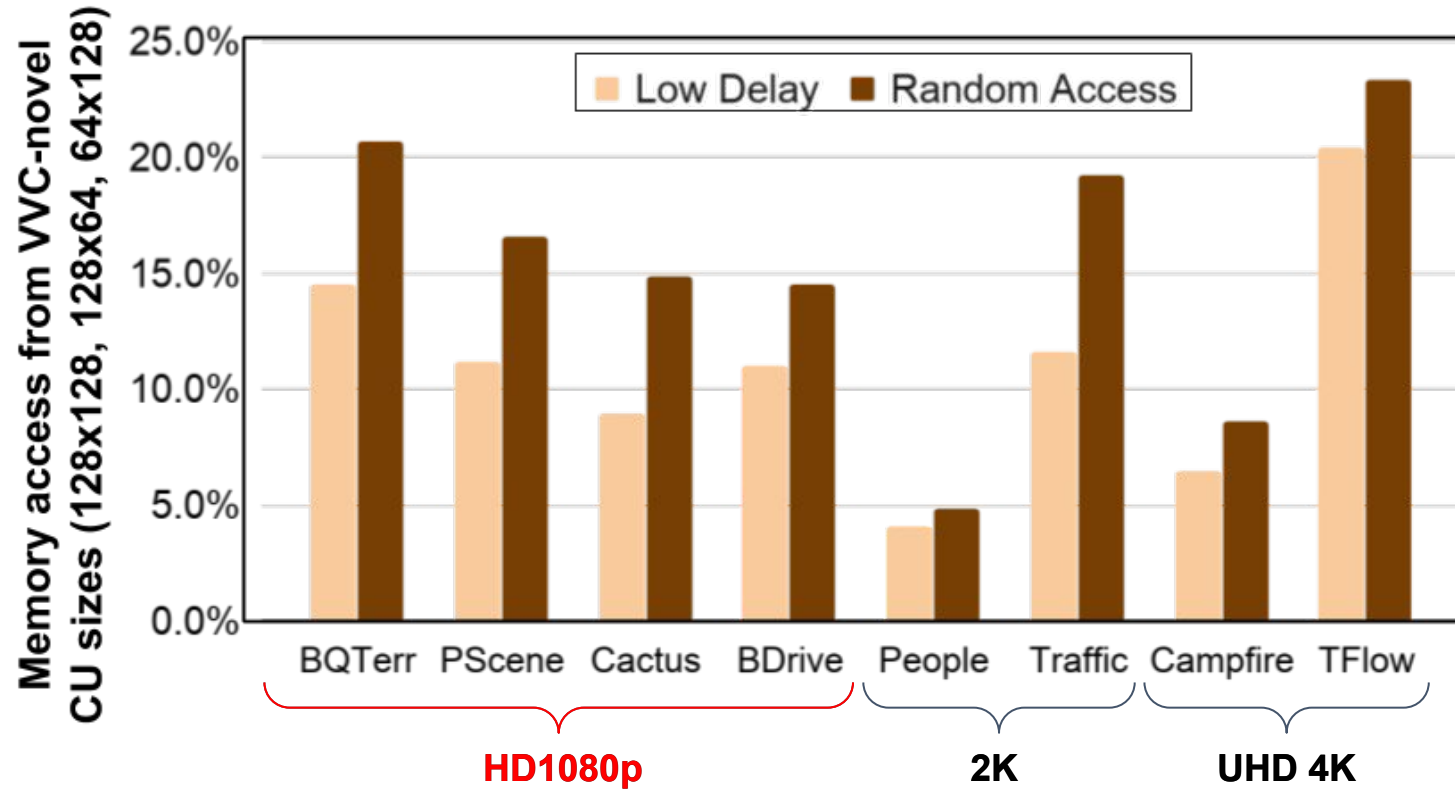
Inter Prediction Specific Memory Analysis

- Analysis-4



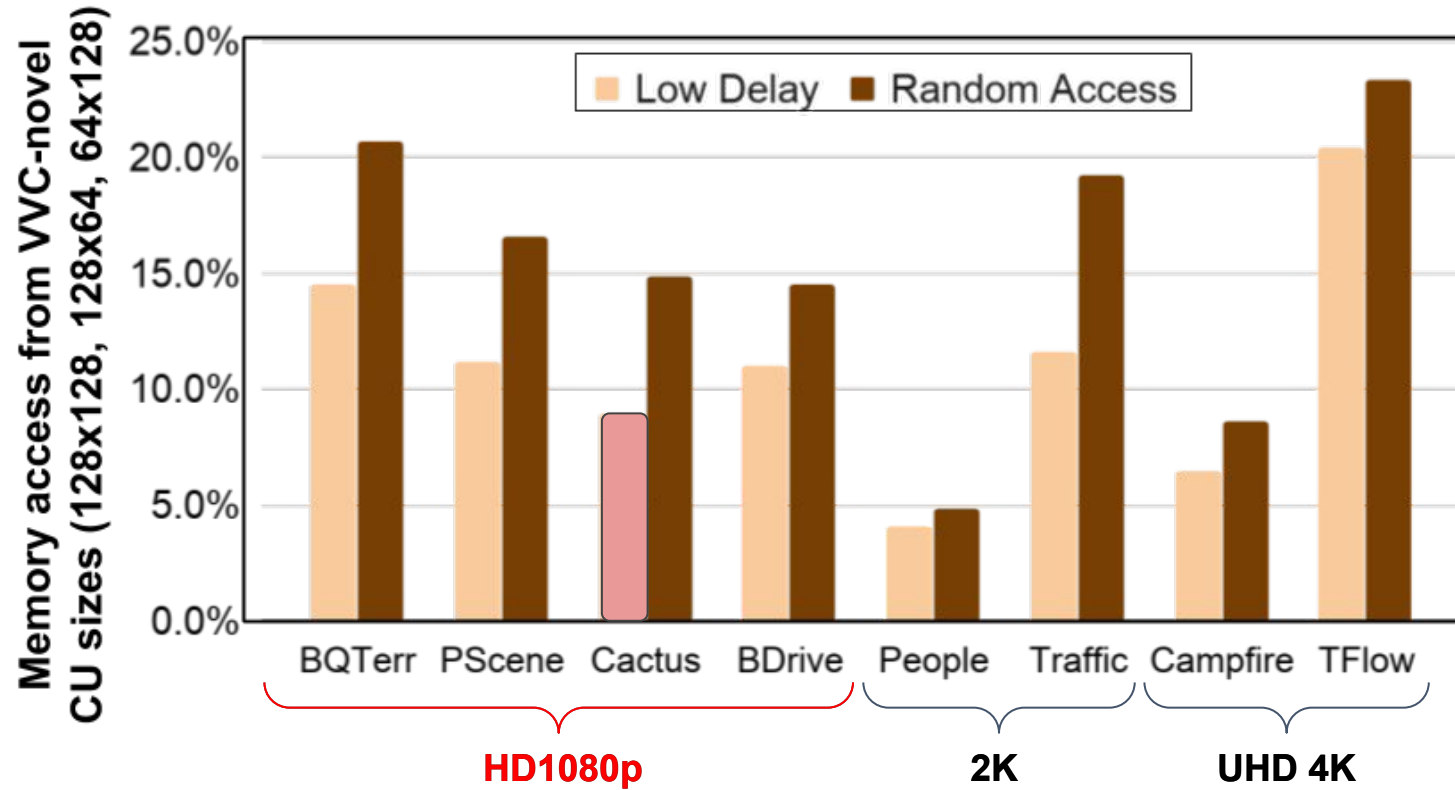
Inter Prediction Specific Memory Analysis

- Analysis-4



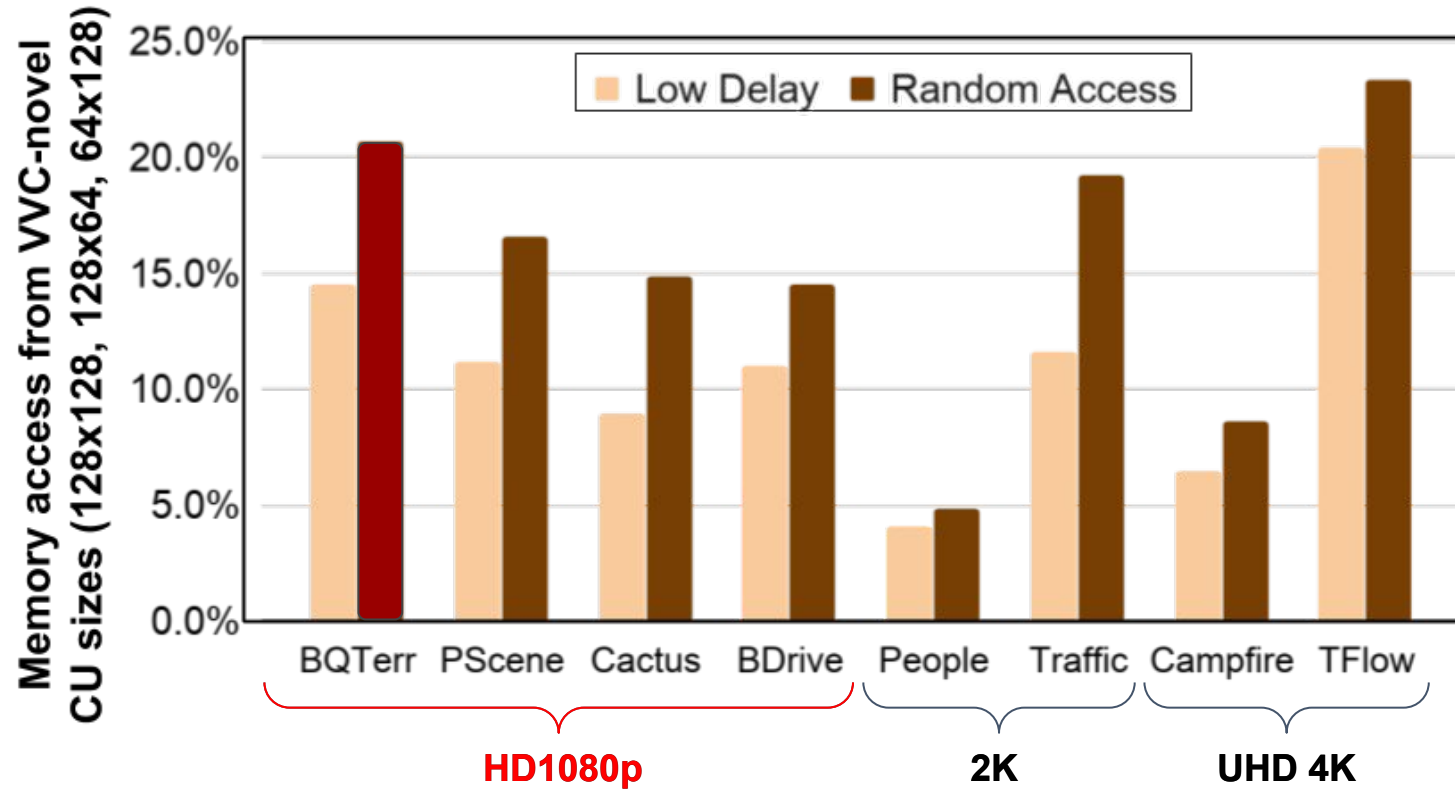
Inter Prediction Specific Memory Analysis

- Analysis-4



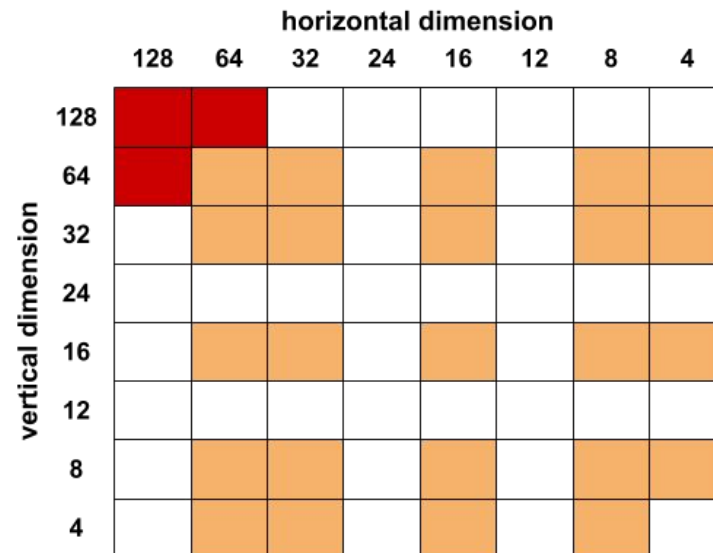
Inter Prediction Specific Memory Analysis

- Analysis-4



Memory Profiling

- Insights from Analysis-4
 - *CUs larger than 64x64 represent up to 23% memory overhead;*
 - *Large CUs memory overhead varies according to video characteristics.*



Conclusions

- Main insights
 - Significant memory accesses increase in VVC;
 - Inter-frame prediction step remains the biggest bottleneck in the encoding process;
 - Novelties introduced in VVC are responsible for most of the memory overhead.
- *There is an open research gap on minimizing the VVC memory bottleneck in order to enable energy-efficient video encoding.*

Conclusions

- Main insights
 - Significant memory accesses increase in VVC;
 - Inter-frame prediction step remains the biggest bottleneck in the encoding process;
 - Novelties introduced in VVC are responsible for most of the memory overhead.
- *There is an open research gap on minimizing the VVC memory bottleneck in order to enable energy-efficient video encoding.*

Conclusions

- Main insights
 - Significant memory accesses increase in VVC;
 - Inter-frame prediction step remains the biggest bottleneck in the encoding process;
 - Novelties introduced in VVC are responsible for most of the memory overhead.
- *There is an open research gap on minimizing the VVC memory bottleneck in order to enable energy-efficient video encoding.*

Conclusions

- Main insights
 - Significant memory accesses increase in VVC;
 - Inter-frame prediction step remains the biggest bottleneck in the encoding process;
 - Novelties introduced in VVC are responsible for most of the memory overhead.
- *There is an open research gap on minimizing the VVC memory bottleneck in order to enable energy-efficient video encoding.*

Conclusions

- Main insights
 - Significant memory accesses increase in VVC;
 - Inter-frame prediction step remains the biggest bottleneck in the encoding process;
 - Novelties introduced in VVC are responsible for most of the memory overhead.
- *There is an open research gap on minimizing the VVC memory bottleneck in order to enable energy-efficient video encoding.*

Conclusions

- Subsequent Works
 - Analyze more recent version of the test models;
 - Consider more higher resolution videos for the analysis.

Conclusions

- Subsequent Works
 - Analyze more recent version of the test models;
 - Consider more higher resolution videos for the analysis.

Conclusions

- Subsequent Works
 - Analyze more recent version of the test models;
 - Consider more higher resolution videos for the analysis.

Memory Assessment of Versatile Video Coding

Thank you!

Arthur Cerveira (UFPel)

Luciano Agostini (UFPel)

Bruno Zatt (UFPel)

Felipe Sampaio (IFRS - Campus Farroupilha)

aacerveira@inf.ufpel.edu.br