FDFlowNet: Fast Optical Flow Estimation using a Deep Lightweight Network

Lingtong Kong, Jie Yang
Institute of Image Processing and Pattern Recognition
Shanghai Jiao Tong University

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1. Introduction

• **Optical Flow Estimation**
  • Optical flow is the pattern of apparent motion of objects, surfaces, and edges in a visual scene caused by the relative motion between an observer and a scene.
  • A fundamental problem in computer vision which plays an important role in many vision applications such as action recognition, video understanding and self-driving cars.

• **Traditional Methods**
  • Variational approaches based on optimizing an energy function with brightness consistency assumption.
  • Weakness:
    • (1) Requires iteration and is computationally heavy.
    • (2) Dissatisfy real-time applications.
    • (3) Not accuracy in large displacement and occluded areas.
1. Introduction

• Deep Learning Methods
  • Given time adjacent image pair and optical flow ground-truth label, training specific deep networks in an end-to-end manner.
  • Advantages:
    • (1) High accuracy in both non-occluded and occluded regions.
    • (2) Fast inference speed aided by advanced parallel computing devices, such as GPU.

Flow diagram of training optical flow network
2. Methods

• Challenges
  • As part of many vision systems, existing optical flow networks usually take considerable computation that slows down running speed.
  • Many optical flow networks have large number of parameters which is not applicable to mobile applications.

• Contributions
  • FDFlowNet: A fast and lightweight deep neural network for efficient optical flow estimation.
    • (1) A compact **U-shape network** for extracting multi-scale context feature compared with existing pyramid network.
    • (2) A **partial fully connected** structure for optical flow decoder that gets a good balance between accuracy and speed.
    • (3) **Cost volume aggregation** and dilated convolution improvement.

Please refer to our paper for network details.
2. Methods

• **U-shape Network**
  
  • The left is traditional pyramid network and the right is our U-shape network.
  
  • A drawback of pyramid network is that the higher resolution where flow estimator locates, the less semantic information corresponding pyramid feature contains. So it takes more convolution layers for the flow estimator to rebuild it. On the other hand, this semantic information is similar among different resolutions, which means that computation on multiple levels can be redundant.

  • The proposed U-shape backbone can fuse better multi-scale information.
  
  • The fused feature is only replacing context feature. And pyramid feature is retained for building cost volume to keep local properties.
2. Methods

• Partial Fully Connected Flow Estimator
  • Comparison of three different modes of flow estimator:
    • Sequential Connected
    • Dense Connected
    • Partial Fully Connected
  • Proposed partial fully connected structure provides a balance and tradeoff between the other two types in model size, computation cost and network performance.

• The last two sequential convolution layers are changed to dilated convolution with dilation rate 2 for enlarging receptive field.
3. Experiments

- **Training Strategy**
  
  All experiments are conducted on one NVIDIA 1080Ti GPU. We implement codes in PyTorch and adopt Adam optimizer. Weight decay is set to $1e^{-4}$ for regularization.

- **FlyingChairs:**
  
  - Batch size 8, Crop size 320 x 480
  - Learning rate 1-300k: $1e^{-4}$, 301-400k: $5e^{-5}$, 401-500k: $2.5e^{-5}$, 501-600k: $1.25e^{-5}$

- **FlyingThings3D:**
  
  - Batch size 4, Crop size 384 x 768
  - Learning rate 1-200k: $1e^{-5}$, 201-300k: $5e^{-6}$, 301-400k: $2.5e^{-6}$, 401-500k: $1.25e^{-6}$

- **MPI Sintel:**
  
  - Batch size 4, 2 from Clean and 2 from Final, Crop size 384 x 768
  - Learning schedule is the same as PWC-Net.

- **KITTI:**
  
  - Batch size 4, 2 from kitti 2012 and 2 from kitti 2015, Crop size 320 x 896
  - Learning schedule is the same as PWC-Net while reducing data augmentation.
3. Experiments

- **Benchmark Results**

<table>
<thead>
<tr>
<th>Method</th>
<th>Sintel Clean</th>
<th>Sintel Final</th>
<th>KITTI12</th>
<th>KITTI15</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>train</td>
<td>test</td>
<td>train</td>
<td>test</td>
</tr>
<tr>
<td>FlowNetC [6]</td>
<td>4.31</td>
<td>7.28</td>
<td>5.87</td>
<td>8.81</td>
</tr>
<tr>
<td>FlowNetC-ft</td>
<td>(3.78)</td>
<td>6.85</td>
<td>(5.28)</td>
<td>8.51</td>
</tr>
<tr>
<td>SPyNet [8]</td>
<td>4.12</td>
<td>6.69</td>
<td>5.57</td>
<td>8.43</td>
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<tr>
<td>SPyNet-ft</td>
<td>(3.17)</td>
<td>6.64</td>
<td>(4.32)</td>
<td>8.36</td>
</tr>
<tr>
<td>FlowNet2 [7]</td>
<td>2.02</td>
<td>3.96</td>
<td>3.14</td>
<td>6.02</td>
</tr>
<tr>
<td>FlowNet2-ft</td>
<td>(1.45)</td>
<td>4.16</td>
<td>(2.01)</td>
<td>5.74</td>
</tr>
<tr>
<td>PWC-Net [9]</td>
<td>2.55</td>
<td>-</td>
<td>3.93</td>
<td>-</td>
</tr>
<tr>
<td>PWC-Net-ft</td>
<td>(2.02)</td>
<td>4.39</td>
<td>(2.08)</td>
<td>5.04</td>
</tr>
<tr>
<td>PWC-Net-small [9]</td>
<td>2.83</td>
<td>-</td>
<td>4.08</td>
<td>-</td>
</tr>
<tr>
<td>PWC-Net-small-ft</td>
<td>(2.27)</td>
<td>5.05</td>
<td>(2.45)</td>
<td>5.32</td>
</tr>
<tr>
<td>LiteFlowNet[10]</td>
<td>2.48</td>
<td>-</td>
<td>4.04</td>
<td>-</td>
</tr>
<tr>
<td>LiteFlowNet-ft</td>
<td>(1.35)</td>
<td>4.54</td>
<td>(1.78)</td>
<td>5.38</td>
</tr>
<tr>
<td>FDFlowNet</td>
<td>2.60</td>
<td>-</td>
<td>4.12</td>
<td>-</td>
</tr>
<tr>
<td>FDFlowNet-ft</td>
<td>(1.80)</td>
<td>3.71</td>
<td>(1.93)</td>
<td>5.11</td>
</tr>
</tbody>
</table>

- Our FDFlowNet ranks the first among listed deep networks on Sintel Clean test, KITTI 2012 test and KITTI 2015 test datasets.
3. Experiments

• Ablation Study
  • "FDFlowNet-U" means that U-shape network is removed. "FDFlowNet-PFC" represents substituting sequential connected structure for partial fully connected structure with dilated convolution.
  • All models are trained on FlyingChairs using the same learning schedule and evaluated on Chairs test, Sintel training datasets.
  • Experiments show that U-shape network can provide better feature representation with fused multi-scale information that obtains an obvious improvement. It is about 5.6% improvement on Sintel Clean and about 5.2% improvement on Sintel Final. Partial fully connected structure with dilated convolution also surpass traditional sequential topology.

<table>
<thead>
<tr>
<th>Variants</th>
<th>FDFlowNet</th>
<th>FDFlowNet-U</th>
<th>FDFlowNet-PFC</th>
</tr>
</thead>
<tbody>
<tr>
<td>Chairs</td>
<td>1.92</td>
<td>2.14</td>
<td>2.02</td>
</tr>
<tr>
<td>Sintel Clean</td>
<td>3.06</td>
<td>3.24</td>
<td>3.18</td>
</tr>
<tr>
<td>Sintel Final</td>
<td>4.23</td>
<td>4.46</td>
<td>4.34</td>
</tr>
</tbody>
</table>
3. Experiments

• Runtime and Parameters
  • It is important for optical flow network to be running fast in real-time and lightweight. This is especially significant in embedded and mobile devices.
  • Experiments are conducted on a machine equipped with one NVIDIA GTX 1080Ti GPU. We use the PyTorch implement of all networks for fair comparison. Running time is obtained on Sintel resolution 436 x 1024 averaged over 1000 times.
  • Our FDFlowNet runs fastest among all the well-behaved models. It is about 2 times faster than PWC-Net and about 3.2 times faster than LiteFlowNet. It also outperforms PWC-Net-small in both running speed and benchmark performance.

<table>
<thead>
<tr>
<th>Model</th>
<th>FlowNetC</th>
<th>FlowNet2</th>
<th>SPyNet</th>
</tr>
</thead>
<tbody>
<tr>
<td>parameters (M)</td>
<td>39.18</td>
<td>162.49</td>
<td>1.20</td>
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<tr>
<td>runtime (ms)</td>
<td>24.6</td>
<td>115.7</td>
<td>47.4</td>
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</table>

<table>
<thead>
<tr>
<th>Model</th>
<th>PWC-Net</th>
<th>LiteFlowNet</th>
<th>FDFlowNet</th>
</tr>
</thead>
<tbody>
<tr>
<td>parameters (M)</td>
<td>8.75</td>
<td>5.37</td>
<td>5.79</td>
</tr>
<tr>
<td>runtime (ms)</td>
<td>32.2</td>
<td>53.2</td>
<td>16.7</td>
</tr>
</tbody>
</table>
3. Experiments

- Video Demos of proposed FDFlowNet

Sintel              KITTI